Gate-Level Minimization

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Presentation Outline

- Boolean Function Minimization
- The Karnaugh Map (K-Map)
- Two, Three, and Four-Variable K-Maps
- Prime and Essential Prime Implicants
- Minimal Sum-of-Products and Product-of-Sums
- Don't Cares
- Five and Six-Variable K-Maps

Multiple Outputs

Boolean Function Minimization

- Complexity of a Boolean function is directly related to the complexity of the algebraic expression
- The truth table of a function is unique
- However, the algebraic expression is not unique
- Boolean function can be simplified by algebraic manipulation
- However, algebraic manipulation depends on experience
- Algebraic manipulation does not guarantee that the simplified Boolean expression is minimal

Example: Sum of Minterms

Truth Table

хуz	f	Minterm	
000	0		Focus on the '1' entries
001	1	$m_1 = x'y'z$	$f - m + m_{-} + m_{-} + m_{-}$
010	1	$m_2 = x'yz'$	$j = m_1 + m_2 + m_3 + m_5 + m_7$
011	1	$m_3 = x'yz$	$f = \sum (1 \ 2 \ 2 \ 5 \ 7)$
100	0		$J = \sum_{i=1}^{n} (1, 2, 3, 3, 7)$
101	1	$m_5 = xy'z$	
110	0		f = x'y'z + x'yz' +
1 1 1	1	$m_7 = xyz$	x'yz + xy'z + xyz

✤ Sum-of-Minterms has 15 literals → Can be simplified

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Algebraic Manipulation

Simplify: f = x'y'z + x'yz' + x'yz + xy'z + xyz (15 literals) (Sum-of-Minterms) f = x'y'z + x'yz' + x'yz + xy'z + xyzf = x'y'z + x'yz + x'yz' + xy'z + xyzReorder f = x'z(y' + y) + x'yz' + xz(y' + y)Distributive \cdot over + f = x'z + x'yz' + xzSimplify (7 literals) $f = x'z + x\overline{z + x'vz'}$ Reorder f = (x' + x)z + x'yz'Distributive \cdot over + f = z + x'yz'Simplify (4 literals) f = (z + x'y)(z + z')Distributive + over · f = z + x'ySimplify (3 literals) Uploaded By: anonymous STUDENTS-HUB.com

Drawback of Algebraic Manipulation

- No clear steps in the manipulation process
 - $\diamond\,$ Not clear which terms should be grouped together
 - ♦ Not clear which property of Boolean algebra should be used next
- Does not always guarantee a minimal expression
 - ♦ Simplified expression may or may not be minimal
 - ♦ Different steps might lead to different non-minimal expressions
- However, the goal is to minimize a Boolean function
- Minimize the number of literals in the Boolean expression
 - ♦ The literal count is a good measure of the cost of logic implementation

Proportional to the number of transistors in the circuit implementation STUDENTS-HUB.com
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Karnaugh Map

- Called also K-map for short
- The Karnaugh map is a diagram made up of squares
- It is a reorganized version of the truth table
- Each square in the Karnaugh map represents a minterm
- Adjacent squares differ in the value of one variable
- Simplified expressions can be derived from the Karnaugh map
 - \diamond By recognizing patterns of squares
- Simplified sum-of-products expression (AND-OR circuits)

Simplified product-of-sums expression (OR-AND circuits)
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Two-Variable Karnaugh Map

• Minterms m_0 and m_1 are adjacent (also, m_2 and m_3)

 \diamond They differ in the value of variable y

• Minterms m_0 and m_2 are adjacent (also, m_1 and m_3)

 \diamond They differ in the value of variable x

Two-variable K-map



Note: adjacent squares horizontally and vertically NOT diagonally STUDENTS-HUB.com

From a Truth Table to Karnaugh Map

- Given a truth table, construct the corresponding K-map
- Copy the function values from the truth table into the K-map
- Make sure to copy each value into the proper K-map square



Two adjacent cells containing 1's can be combined



✤ Therefore, *f* can be simplified as: f = x + y' (2 literals)

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Two-variable Map - Example 2

Example

	x y	F	Minterm		
0	00	0	m ₀	<i>x'y'</i>	
1	0 1	1	<i>m</i> ₁	x'y	
2	1 0	1	<i>m</i> ₂	<i>x y</i> ′	
3	1 1	1	<i>m</i> ₃	xy	







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Three-Variable Karnaugh Map

- ✤ Have eight squares (for the 8 minterms), numbered 0 to 7
- The last two columns are not in numeric order: 11, 10
 - $\diamond\,$ Remember the numbering of the squares in the K-map
- Each square is adjacent to three other squares
- Minterms in adjacent squares can always be combined
 This is the key idea that makes the K-map work
- Labeling of rows and columns is also useful



Simplify the Boolean function: $f(x, y, z) = \sum (3, 4, 5, 7)$

- f = x'yz + xy'z' + xy'z + xyz (12 literals)
- 1. Mark '1' all the K-map squares that represent function f



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Here is a second example: $f(x, y, z) = \sum (3, 4, 6, 7)$

f = x'yz + xy'z' + xyz' + xyz (12 literals)

Learn the locations of the 8 indices based on the variable order

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Combining Squares on a 3-Variable K-Map

- By combining squares, we reduce number of literals in a product term, thereby reducing the cost
- On a 3-variable K-Map:
 - ♦ One square represents a minterm with 3 variables
 - ♦ Two adjacent squares represent a term with 2 variables
 - ♦ Four adjacent squares represent a term with 1 variable

Eight adjacent square is the constant '1' (no variables)

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Example: Simplify the Boolean function $F(x, y, z) = \Sigma(2,3,4,5) = x'y + xy'$

	x y z	F	Minterm	yz
0	000	0	$m_0 x'y'z'$	x' y' z' x' y' z x' y z x' y z'
1	001	0	$m_1 x'y'z$	$\frac{1}{xy'z'} \frac{xy'z}{xy'z} \frac{xyz}{xyz'}$
2	010	1	$m_2 x'yz'$	
3	011	1	$m_3 x'yz$	$\begin{bmatrix} y z & y \\ y & 00 & 01 & 11 & 10 \end{bmatrix}$
4	100	1	m_4 xy'z'	
5	101	1	$m_5 xy'z$	
6	1 1 0	0	$m_6 xyz'$	
7	111	0	m_7 xyz	
NTS	-HUB.com	-		F = xy + xy Uploaded By: and

- ♦ Consider the Boolean function: $f(x, y, z) = \sum (2, 3, 5, 6, 7)$
- f = x'yz' + x'yz + xy'z + xyz' + xyz
- The four minterms that form the 2×2 red square are reduced to the term y
- The two minterms that form the blue rectangle are reduced to the term xz

***** Therefore:
$$f = y + xz$$



Consider the function: $f(x, y, z) = \sum (0, 1, 2, 4, 6, 7)$

Find a minimal sum-of-products (SOP) expression



Minimal sum-of-products: f = z' + x'y' + xy (5 literals)

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Map for
$$F(x,y,z) = \Sigma(0,2,4,5,6) = z' + xy'$$



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Example:									у			
			_			l		0	1		0	
	x y	' Z	F	Mi	nterm		r	0		1	0	
0	00	0	0	<i>m</i> ₀	<i>x'y'z'</i>		Λ	v		7	v	
1	00) 1	1	<i>m</i> ₁	x'y'z	F(x, y, z)	z) :	= x'y'	z + x'	yz + x	y'z + z	xyz
2	0 1	. 0	0	<i>m</i> ₂	<i>x'yz'</i>			\overline{r} 7 ($\sqrt{\frac{1}{2}}$	r	$\sqrt{\frac{1}{2}}$	- 12)
3	01	1	1	<i>m</i> ₃	x'yz			\overline{x}	y - y) 7	\mathcal{A}	$\frac{x}{x}$	<i>y</i>)
4	1 0	0	0	<i>m</i> ₄	xy'z'					2		
5	1 0) 1	1	<i>m</i> ₅	xy'z		[0		J	2	
6	1 1	. 0	0	<i>m</i> ₆	xyz'			U				
7	1 1	1	1	<i>m</i> ₇	xyz		x	0	1		0	
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F(x	(y,z) =	$\sum (0$),2,4,	5,6)		<i>m</i> ₀	<i>m</i> ₁	<i>m</i> ₃	<i>m</i> ₂
	x y z	F	Minterm			<i>m</i> ₄	<i>m</i> ₅	<i>m</i> ₇	<i>m</i> ₆
0	000	1	<i>m</i> ₀	<i>x'y'z'</i>	$x \xrightarrow{y z}$	00	01	11	10
1	001	0	<i>m</i> ₁	x'y'z	0	x'y'z'	x'y'z	x' vz	x'vz'
2	0 1 0	1	<i>m</i> ₂	<i>x'yz'</i>	1	$\frac{y}{xy'z'}$	$\frac{y'}{xy'z}$	XVZ.	$\frac{y}{xyz'}$
3	0 1 1	0	<i>m</i> ₃	x'yz	-	~~~~~			
4	100	1	<i>m</i> ₄	xy'z'	Г		0	y o	
5	101	1	<i>m</i> ₅	xy'z	_		0	0	
6	1 1 0	1	<i>m</i> ₆	xyz'	x	S	D	0	1
7	111	0	<i>m</i> ₇	xyz			Z		
			-		F =	z' +	xy'		

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Let the Boolean Function

$$F = A'C + A'B + AB'C + BC$$
(a) Express this function as a sum of minterms

$$= A'(B + B')C + A'B(C + C') + AB'C + (A + A')BC$$

$$= A'BC + A'B'C + A'BC + A'BC' + AB'C + ABC + A'BC + A'BC + A'BC + A'BC + A'BC' + AB'C + ABC = \sum (1,2,3,5,7)$$

$$ABC = 00 \quad 01 \quad 11 \quad 10 \quad A'B + (1,1,3,5,7)$$

 $A \{ 1$

C

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Simplify the following Boolean function, using three-variable maps

F

$$(x, y, z) = xy + x'y'z' + x'yz'$$

= $xy(z + z') + x'y'z' + x'yz'$
= $xyz + xyz' + x'y'z' + x'yz'$
= $\sum (0, 2, 6, 7)$



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Four-Variable Karnaugh Map

4 variables \rightarrow 16 squares

Remember the numbering of the squares in the K-map

Each square is adjacent to four other squares

$$\begin{array}{lll} m_0 &= w'x'y'z' & m_1 &= w'x'y'z \\ m_2 &= w'x'yz' & m_3 &= w'x'yz \\ m_4 &= w'xy'z' & m_5 &= w'xy'z \\ m_6 &= w'xyz' & m_7 &= w'xyz \\ m_8 &= wx'y'z' & m_9 &= wx'y'z \\ m_{10} &= wx'yz' & m_{11} &= wx'yz \\ m_{12} &= wxy'z' & m_{13} &= wxy'z \\ m_{14} &= wxyz' & m_{15} &= wxyz \\ \end{array}$$

Notice the order of Rows 11 and 10 and the order of columns 11 and 10



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Combining Squares on a 4-Variable K-Map

On a 4-variable K-Map:

 \diamond One square represents a minterm with 4 variables

♦ Two adjacent squares represent a term with 3 variables

♦ Four adjacent squares represent a term with 2 variables

♦ Eight adjacent squares represent a term with 1 variable

♦ Combining all 16 squares is the constant '1' (no variables)

Combining Eight Squares



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Combining Four Squares



Combining Two Squares



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Given $f(w, x, y, z) = \sum (0, 2, 4, 5, 6, 7, 8, 12)$



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*****Example

Simplify: F = A'B'C' + B'CD' + A'BCD' + AB'C'



 $F = \frac{B'D'}{B'C'} + \frac{B'C'}{A'CD'}$

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Next . . .

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- Don't Cares
- Five and Six-Variable K-Maps

Multiple Outputs

Prime Implicants

- Prime Implicant: a product term obtained by combining the maximum number of adjacent squares in the K-map
- The number of combined squares must be a power of 2
- Essential Prime Implicant: is a prime implicant that covers at least one minterm not covered by the other prime implicants
- The prime implicants and essential prime implicants can be determined by inspecting the K-map

Prime implicants

When choosing adjacent squares in a map, make sure that:

- 1. All the minterms are covered when combining the squares
- 2. The number of terms in the expression is minimized
- 3. There are no redundant terms (minterms already covered by other terms)

Example of Prime Implicants

Find all the prime implicants and essential prime implicants for: $f(a, b, c, d) = \sum (0, 2, 3, 5, 7, 8, 9, 10, 11, 13, 15)$


Prime Implicant – Example 2

Find all prime implicants for

 $F(A, B, C, D) = \sum (0,2,3,8,9,10,11,12,13,14,15)$



Simplification Procedure Using the K-Map

- 1. Find all the essential prime implicants
 - ♦ Covering maximum number (power of 2) of 1's in the K-map
 - ♦ Mark the minterm(s) that make the prime implicants essential
- 2. Add prime implicants to cover the function
 - \diamond Choose a minimal subset of prime implicants that cover all remaining 1's
 - ♦ Make sure to cover all 1's not covered by the essential prime implicants
 - ♦ Minimize the overlap among the additional prime implicants
- Sometimes, a function has multiple simplified expressions

 \diamond You may be asked to list all the simplified sum-of-product expressions

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Obtaining All Minimal SOP Expressions

Consider again: $f(a, b, c, d) = \sum (0, 2, 3, 5, 7, 8, 9, 10, 11, 13, 15)$

Obtain all minimal sum-of-products (SOP) expressions



Two essential Prime

Implicants: bd and b'd'

Four possible solutions: f = bd + b'd' + cd + ad f = bd + b'd' + cd + ab' f = bd + b'd' + b'c + ab' f = bd + b'd' + b'c + ad

Simplification Example 2

Simplify F(A, B, C, D) given on the K-map



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Product-of-Sums (POS) Simplification

- All previous examples were expressed in Sum-of-Products form
- With a minor modification, the Product-of-Sums can be obtained
- ♦ Example: $f(a, b, c, d) = \sum (1, 2, 3, 9, 10, 11, 13, 14, 15)$



Product-of-Sums Simplification Procedure

- 1. Draw the K-map for the function f
 - \diamond Obtain a minimal Sum-of-Products (SOP) expression for *f*
- 2. Draw the K-map for f', replacing the 0's of f with 1's in f'
- 3. Obtain a minimal Sum-of-Products (SOP) expression for f'
- 4. Use DeMorgan's theorem to obtain f = (f')'
 - \diamond The result is a minimal Product-of-Sums (POS) expression for *f*
- 5. Compare the cost of the minimal SOP and POS expressions
 - \diamond Count the number of literals to find which expression is minimal

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Product of Sums Simplification-example 2



Product-Of-Sums Simplification - Example 3

$$F(A, B, C, D) = \sum (0, 1, 2, 5, 8, 9, 10)$$

F = B'D' + B'C' + A'C'D

Take the squares with zeros and obtain the simplified complemented function

F' = AB + CD + BD'

Complement the above expression and use DeMorgan's

$$F = (A' + B')(C' + D')(B' + D)$$





Note: BC'D' + BCD' = BD'



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Don't Cares

Five and Six-Variable K-Maps

Multiple Outputs

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Don't Cares

Sometimes, a function table may contain entries for which:

- \diamond The input values of the variables will never occur, or
- \diamond The output value of the function is never used
- In this case, the output value of the function is not defined
- The output value of the function is called a don't care
- A don't care is an X value that appears in the function table
- The X value can be later chosen to be 0 or 1

 \diamond To minimize the function implementation

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Example of a Function with Don't Cares

- \clubsuit Consider a function *f* defined over BCD inputs
- The function input is a BCD digit from 0 to 9
- The function output is 0 if the BCD input is 0 to 4
- The function output is 1 if the BCD input is 5 to 9
- The function output is X (don't care) if the input is 10 to 15 (not BCD)

Truth Table

а	b	С	d	f
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	0
0	1	0	0	0
0	1	0	1	1
0	1	1	0	1
0	1	1	1	1
1	0	0	0	1
1	0	0	1	1
1	0	1	0	Х
1	0	1	1	Х
1	1	0	0	Х
1	1	0	1	Х
1	1	1	0	Х
1	1	1	1	Х

Don't-Care Condition



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Don't-Care Condition



Don't-Care Condition - Example 1



Minimizing Functions with Don't Cares - Example 2

Consider: $f = \sum_{m} (5, 6, 7, 8, 9) + \sum_{d} (10, 11, 12, 13, 14, 15)$

If the don't cares were treated as 0's we get:

- f = a'bd + a'bc + ab'c' (9 literals)
- If the don't cares were treated as 1's we get:
- f = a + bd + bc (5 literals)

The don't care values can be selected to be either 0 or 1, to produce a minimal expression



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Simplification Procedure with Don't Cares

- 1. Find all the essential prime implicants
 - ♦ Covering maximum number (power of 2) of 1's and X's (don't cares)
 - ♦ Mark the 1's that make the prime implicants essential
- 2. Add prime implicants to cover the function
 - ♦ Choose a minimal subset of prime implicants that cover all remaining 1's
 - ♦ Make sure to cover all 1's not covered by the essential prime implicants
 - ♦ Minimize the overlap among the additional prime implicants
 - ♦ You need not cover all the don't cares (some can remain uncovered)
- Sometimes, a function has multiple simplified expressions

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Minimizing Functions with Don't Cares - Example 3

Simplify:
$$g = \sum_{m} (1, 3, 7, 11, 15) + \sum_{d} (0, 2, 5)$$

Solution 1: g = cd + a'b' (4 literals)

Solution 2: g = cd + a'd (4 literals)





Not all don't cares need be covered

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Minimal Product-of-Sums with Don't Cares

Simplify:
$$g = \sum_{m} (1, 3, 7, 11, 15) + \sum_{d} (0, 2, 5)$$

Obtain a product-of-sums minimal expression

Solution: $g' = \sum_{m} (4, 6, 8, 9, 10, 12, 13, 14) + \sum_{d} (0, 2, 5)$

Minimal g' = d' + ac' (3 literals)

Minimal product-of-sums:

g = d(a' + c) (3 literals)

The minimal sum-of-products expression for g had 4 literals

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Multiple Outputs

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Five-Variable Karnaugh Map

- Consists of $2^5 = 32$ squares, numbered 0 to 31
 - \diamond Remember the numbering of squares in the K-map
- Can be visualized as two layers of 16 squares each
- ✤ Top layer contains the squares of the first 16 minterms (a = 0)
- Bottom layer contains the squares of the last 16 minterms (a = 1)

de)	<i>a</i> =	= 0		de $a = 1$							
bc	00	01	11	10	bc	00	01	11	10			
00	m_0	m_1	m_3	m_2	00	<i>m</i> ₁₆	<i>m</i> ₁₇	<i>m</i> ₁₉	m_{18}			
01	m_4	m_5	m_7	m_6	01	<i>m</i> ₂₀	<i>m</i> ₂₁	m ₂₃	m ₂₂			
11	<i>m</i> ₁₂	<i>m</i> ₁₃	<i>m</i> ₁₅	m_{14}	11	m ₂₈	<i>m</i> ₂₉	<i>m</i> ₃₁	m_{30}			
10	m_8	m_9	m_{11}	m_{10}	10	<i>m</i> ₂₄	m ₂₅	m ₂₇	m ₂₆			

Each square is adjacent to **5** other squares: **4** in the same layer and **1** in the other layer: m_0 is adjacent to m_{16} m_1 is adjacent to m_{17} m_4 is adjacent to m_{20} ...

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Five-Variable K-Map - Example 1

Given: $f(a, b, c, d, e) = \sum (0, 1, 8, 9, 16, 17, 22, 23, 24, 25)$

Draw the 5-Variable K-Map

Obtain a minimal Sum-of-Products expression for f

Solution: f = c'd' + ab'cd (6 literals)



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Five-Variable K-Map - Example 3

 $g(a, b, c, d, e) = \sum_{m} (3, 6, 7, 11, 24, 25, 27, 28, 29) + \sum_{d} (2, 8, 9, 12, 13, 26)$

Draw the 5-Variable K-Map

Obtain a minimal Sum-of-Products expression for g

Solution: g = bd' + a'b'd + bc'e (8 literals)



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Six-Variable Karnaugh Map

• Consists of $2^6 = 64$ squares, numbered 0 to 63

Can be visualized as four layers of 16 squares each

 \diamond Four layers: ab = 00, 01, 11, 10 (Notice that layer 11 comes before 10)

Each square is adjacent to 6 other squares:

 \diamond 4 squares in the same layer and 2 squares in the above and below layers

ab = 00				ab = 01			ab = 11				ab = 10					
cd	00	01	11	10	00	01	11	10	00	01	11	10	00	01	11	10
00	m_0	m_1	m_3	m_2	m_{16}	m_{17}	<i>m</i> ₁₉	m_{18}	m_{48}	m_{49}	m_{51}	m_{50}	m_{32}	m_{33}	<i>m</i> ₃₅	m_{34}
01	m_4	m_5	m_7	m_6	m_{20}	<i>m</i> ₂₁	<i>m</i> ₂₃	<i>m</i> ₂₂	m_{52}	m_{53}	m_{55}	m_{54}	m_{36}	m ₃₇	m ₃₉	m_{38}
11	<i>m</i> ₁₂	<i>m</i> ₁₃	<i>m</i> ₁₅	m_{14}	m_{28}	m ₂₉	<i>m</i> ₃₁	m_{30}	m_{60}	m_{61}	m ₆₃	m_{62}	m_{44}	m_{45}	m_{47}	m_{46}
10	m_8	m_9	<i>m</i> ₁₁	<i>m</i> ₁₀	<i>m</i> ₂₄	<i>m</i> 25	<i>m</i> ₂₇	<i>m</i> ₂₆	m_{56}	m_{57}	m_{59}	m_{58}	m_{40}	m_{41}	<i>m</i> ₄₃	<i>m</i> ₄₂

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6- Variable K-Maps - Example 1



6- Variable K-Maps - Example 1



6 Variable K-Maps – Example 2

 $h(a, b, c, d, e, f) = \sum (2, 10, 11, 18, 21, 23, 29, 31, 34, 41, 50, 53, 55, 61, 63)$

Draw the 6-Variable K-Map

Obtain a minimal Sum-of-Products expression for *h*

Solution: h = c'd'ef' + b d f + a'b'c d'e + a b c d'e'f (18 literals)



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Multiple Outputs

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Multiple Outputs

- Suppose we have two functions: f(a, b, c) and g(a, b, c)
- Same inputs: a, b, c, but two outputs: f and g
- We can minimize each function separately, or
- \clubsuit Minimize f and g as one circuit with two outputs
- \clubsuit The idea is to share terms (gates) among f and g



Two separate circuits STUDENTS-HUB.com



One circuit with Two Outputs

Multiple Outputs: Example 1

Given: $f(a, b, c) = \sum (0, 2, 6, 7)$ and $g(a, b, c) = \sum (1, 3, 6, 7)$

Minimize each function separately Minimize both functions as one circuit





Multiple Outputs: Example 2

 $f(a, b, c, d) = \sum (3, 5, 7, 10, 11, 14, 15), g(a, b, c, d) = \sum (1, 3, 5, 7, 10, 14)$

Draw the K-map and write minimal SOP expressions of f and g

f = a'bd + ac + cd g = a'd + acd'

Extract the common terms of f and g



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Common Terms -> Shared Gates

Minimal f = a'bd + ac + cd Minimal g = a'd + acd'Let $T_1 = a'd$ and $T_2 = ac$ (shared by f and g) Minimal $f = T_1 b + T_2 + cd$, Minimal $g = T_1 + T_2 d'$



NO Shared Gates

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One Circuit Two Shared Gates

Quine-McCluskey Method

- The Karnaugh map method described in Unit 5 is an effective way to simplify switching functions which have a small number of variables
- A systematic solution to K-Map when more complex function with more literals is given
- In principle, can be applied to an arbitrary large number of inputs
- One can translate Quine-McCluskey method into a computer program to perform minimization

Universal Gates

- NAND and NOR gates are said to be *universal* gate because any logic circuit can be implemented with it.
 - Digital circuits are frequently constructed with NAND or NOR gates rather than with AND and OR gates.
- NAND and NOR gates are easier to fabricate with electronic components and are the basic gates used in all IC digital logic families.
- Because of the prominence of NAND and NOR gates in the design of digital circuits, rules and procedures have been developed for the conversion from Boolean functions given in terms of AND, OR, and NOT into equivalent NAND and NOR logic diagrams

Universal Gates



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NAND Gate Symbol

Two equivalent graphic symbols for the NAND gate

- The AND-invert symbol has been defined previously and consists of an AND graphic symbol followed by a small circle negation indicator referred to as a bubble.
- Alternatively, it is possible to represent a NAND gate by an OR graphic symbol that is preceded by a bubble in each input.



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Two-Level NAND Implementation

- A convenient way to implement a Boolean function with NAND gates is to obtain the simplified Boolean function in terms of Boolean operators and then convert the function to NAND logic.
- The implementation of Boolean functions with NAND gates requires that the functions be in sum-of-products form.
- ✤ Procedure:
 - Oraw a NAND gate for each product term of the expression that has at least two literals. The inputs to each NAND gate are the literals of the term. This procedure produces a group of first-level gates.
 - Oraw a single gate using the AND-invert or the invert-OR graphic symbol in the second level, with inputs coming from outputs of first-level gates.
 - A term with a single literal requires an inverter in the first level. However, if the single literal is complemented, it can be connected directly to an input of the second level NAND gate.
Two-Level NAND Implementation

F = AB + CD



(a)



(b)



F = ((AB)'(CD)')' = AB + CD

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Boolean function with NAND gates

Implement the following Boolean function with NAND gates:

F(x, y, z) = (1, 2, 3, 4, 5, 7)





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Multilevel NAND Implementation

- General Procedure for converting a multilevel AND– OR diagram into an all-NAND diagram using mixed notation is as follows:
 - Convert all AND gates to NAND gates with AND-invert graphic symbols.
 - Convert all OR gates to NAND gates with invert-OR graphic symbols.
 - Check all the bubbles in the diagram. For every bubble that is not compensated by another small circle along the same line, insert an inverter (a one-input NAND gate) or complement the input literal.

Multilevel NAND Implementation





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(b) NAND gates

Multilevel NAND Implementation

• Consider the function F = (AB'+A'B)E(C+D')

Note: an inverter is added to three paths



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NOR Gates

The NOR operation is the dual of the NAND operation. Therefore, all procedures and rules for NOR logic are the duals of the corresponding procedures and rules developed for NAND logic.



NOR Gate Symbol

The two graphic symbols for the mixed notation.

- The OR-invert symbol defines the NOR operation as an OR followed by a complement.
- The invert-AND symbol complements each input and then performs an AND operation.



Two-Level NOR Implementation

Rules for 2-Level NOR Implementations

- ♦ Simplify the function and express it in product of sums form
- Draw a NOR gate (using OR-NOT symbol) for each sum term (with 2 literals or more)
- ♦ Draw a single NOR gate (using NOT-AND symbol) the 2nd level (in place of the AND gate)
- ♦ A term with single literal requires a NOT



Two-Level NOR Implementation

 Consider F = Σm(1,2,3,5,7) – Implement using NOR gates







Rules for Multi-Level NOR Implementations

- ✤ NOTE: the function is NOT in the standard form WHY?
- Steps:
 - ♦ Draw a NOR (OR-NOT) gate for each OR gate
 - ♦ Draw a NOR (NOT-AND) gate for each AND gate
 - ♦ Check paths add inverters to make even number of bubbles
 - Consider the function F = (AB'+A'B)E(C+D')



Multilevel NOR Implementation

• Consider the function F = (AB)'+C



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Multilevel NOR Implementation

Consider the function F = (A+B)'C





$$F2 = (A+B)''C''$$

= (A+B)'C'
= (A+B)' C
= (A+B)' C

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А

Exclusive OR / Exclusive NOR

- Exclusive OR (XOR) is an important Boolean operation used extensively in logic circuits
- Exclusive NOR (XNOR) is the complement of XOR

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XOR / XNOR Functions

- ✤ The XOR function is: $x \oplus y = xy' + x'y$
- ✤ The XNOR function is: $(x \oplus y)' = xy + x'y'$
- ✤ XOR and XNOR gates are complex
 - \diamond Can be implemented as a true gate, or by
 - ♦ Interconnecting other gate types
- XOR and XNOR gates do not exist for more than two inputs
 - ♦ For 3 inputs, use two XOR gates
 - ♦ The cost of a 3-input XOR gate is greater than the cost of two XOR gates
- Uses for XOR and XNOR gates include:
 - ♦ Adders, subtractors, multipliers, counters, incrementers, decrementers

Parity generators and checkers STUDENTS-HUB.com

XOR Implementations



(b) Exclusive-OR with NAND gates $(x' + y')x + (x' + y')y = xy' + x'y = x \oplus y$ Uploaded By: anonymous

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XOR and XNOR Properties

- $x \oplus 0 = x x \oplus 1 = x'$ $x \oplus x = 0 x \oplus x' = 1$
- $x \oplus y = y \oplus x$
- $x' \oplus y' = x \oplus y$

$$\bigstar (x \oplus y)' = x' \oplus y = x \oplus y'$$

XOR and XNOR are **associative** operations

$$\bigstar (x \oplus y) \oplus z = x \oplus (y \oplus z) = x \oplus y \oplus z$$

$$\bigstar \left((x \oplus y)' \oplus z \right)' = (x \oplus (y \oplus z)')' = x \oplus y \oplus z$$

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Odd Function

- Output is 1 if the number of 1's is odd in the inputs
- Output is the XOR operation on all input variables

	X	у	z	fodd
Odd Function with 3 inputs	0	0	0	0
	0	0	1	1
	0	1	0	1
	0	1	1	0
	1	0	0	1
	1	0	1	0
	1	1	0	0
	1	1	1	1

$$fodd = \sum (1, 2, 4, 7)$$

$$fodd = x'y'z + x'yz' + xy'z' + xyz$$



Implementation using two XOR gates

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Even Function

W	X	У	Z	feven
0	0	0	0	1
0	0	0	1	0
0	0	1	0	0
0	0	1	1	1
0	1	0	0	0
0	1	0	1	1
0	1	1	0	1
0	1	1	1	0
1	0	0	0	0
1	0	0	1	1
1	0	1	0	1
1	0	1	1	0
1	1	0	0	1
1	1	0	1	0
1	1	1	0	0
1	1	1	1	1

- Output is 1 if the number of 1's is even in the inputs (complement of odd function)
- Output is the XNOR operation on all inputs

$$feven = \sum (0, 3, 5, 6, 9, 10, 12, 15)$$

$$feven = (w \oplus x \oplus y \oplus z)'$$



Implementation using two XOR gates and one XNOR

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Even Function with 4 inputs

Parity Generators and Checkers



- ✤ A parity bit is added to the *n*-bit code
 - \Rightarrow Produces (*n*+1)-bit code with an odd (or even) count of 1's
- Odd parity: count of 1's in the (n+1)-bit code is odd
 - ♦ Use an even function to generate the odd parity bit
 - \diamond Use an **even function** to check the (*n*+1)-bit code
- Even parity: count of 1's in the (*n*+1)-bit code is even
 - ♦ Use an odd function to generate the even parity bit

Use an odd function to check the (n+1)-bit code STUDENTS-HUB.com

Example of Parity Generator and Checker

- Design even parity generator & checker for 3-bit codes
- Solution:
 - ♦ Use 3-bit odd function to generate even parity bit *P*.
 - $\Rightarrow \quad \text{Use 4-bit odd function to check if} \\ \text{there is an error } E \text{ in even parity.}$
 - ♦ Given that: xyz = 001 then P = 1.
 The sender transmits Pxyz = 1001.
 - ♦ If y changes from 0 to 1 between generator and checker, the parity checker receives Pxyz = 1011 and produces E = 1, indicating an error.

