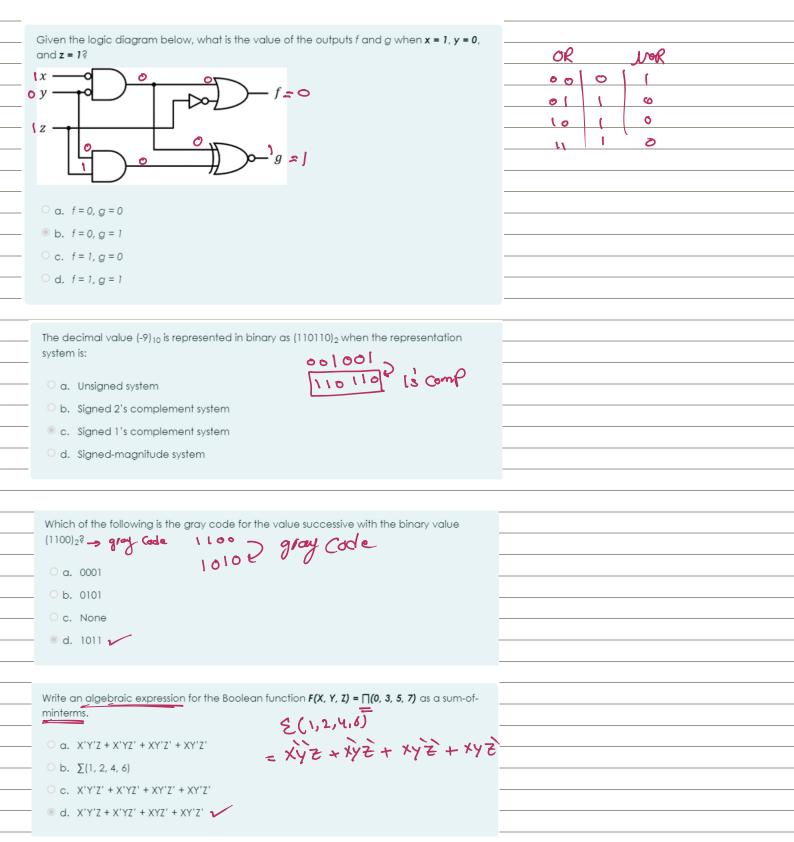


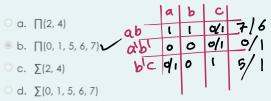
Format on Ch.1 + Ch.2

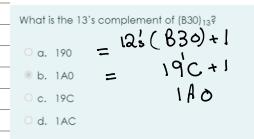
By Rawan Alfares

كلُّ محاولاتك عند الله أجور استعن بالله ولا تعجز



Which of the following canonical forms represents the complement of the Boolean function f(a, b, c) = ab + a'b' + b'c?





Given the following Boolean functions F and G:

$$F(X, Y, Z) = \sum (0, 4, 6) \longrightarrow F' = \mathcal{E}(1, 2, 3, 5, 7)$$

$$G(X, Y, Z) = \prod (1, 3, 6) \longrightarrow G = \mathcal{E}(0, 2, 1, 15, 7)$$
Express the function $F' \cdot G$ as a sum-of-minterms.

as a product of Max terms.

- a. ∑(2, 5, 7)
- \bigcirc b. $\Sigma(0, 1, 2, 3, 4, 5, 7)$
- \circ c. $\Sigma(1, 2, 3, 5, 6, 7)$
- d. $\sum (2, 4)$

Using Boolean Algebraic manipulations, the Boolean function F(X, Y, Z) = X'(Y' + Z) + XZ can be simplified to F = X'Y' + Z. This is achieved using which of the following sequences of theorems? (1) distribution

(2) distri

- a. Distribution, Simplification, and then Absorption
- = xy+ z.1 b. Distribution, Distribution, Complement, and then Identity
- O c. Distribution, Complement, and then Identity
- Od. Distribution, and then Consensus

Carry out the following conversion $(17.1)_8 = (?)_4$

- a. 33.01
- ob. None
- o. 15.125
- ⊚ d. 33.02√

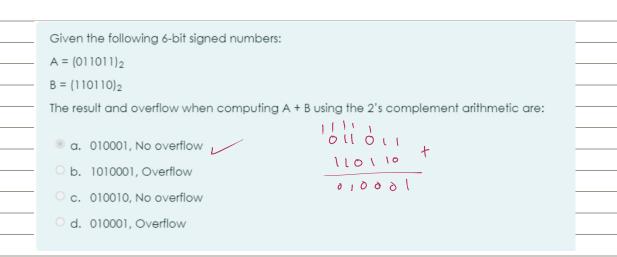
0.125 x4 0.500

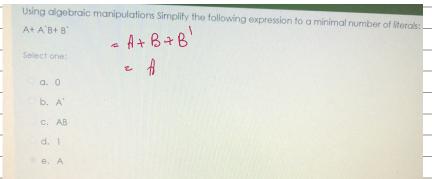
In signed 1's complement representation with 3-bits, what is the largest positive number that can be added to (-1) without causing an overflow?

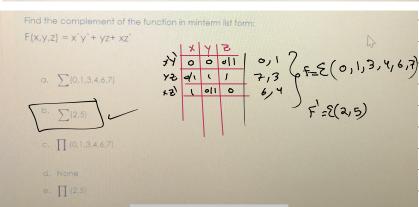
- oa. 4
- (b. 3 oc. 2
- od. 1

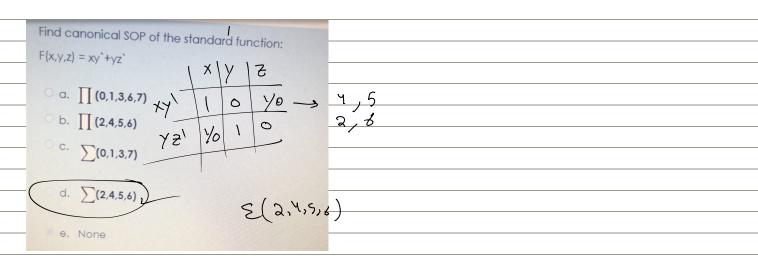
الأوفر فلو مستحيل يهم بين في و فياخ أكبر اقه هن الطع بحيث الما الله عارة e Hide - abit

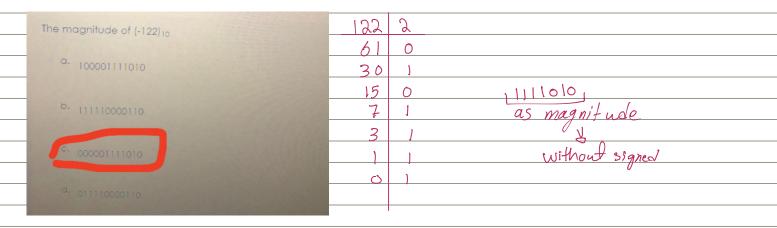












Perform the following operation (101101)₂ = (110110)₂ = (2)₃ using 6-bit 2's complement representation?

o
$$10011$$

o 10011

110011

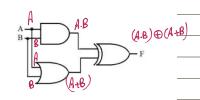
52) What is the output of the following circuit?

a. AB

b. A+B

c. A'B'+AB

d. A'B+AB'



Let
$$(A.B) = y$$
 $y \oplus d$

Let $(A+B) = d$
 $(A.B)(AB) + (A+D)(A'+B')$
 $= (AB)(AB) + (AB)(A'+B')$
 $= (AB) + AB + BA' + (BB)$
 $= (AB) + A'B + (AB)(A'+B')$

51) The shown circuit can be implemented using a minimum of:

- a. 3 NAND Gates
- b. 4 NAND Gates
- c. 5 NAND Gates
- d. 4 NAND Gates and 1 NOR Gate

