

FACULATY OF ENGINEERING AND TECHNOLOGY DEPARTMENT OF ELECTRICAL AND COMPUTER ENGINEERING

ENEE 2101

Circuits Laboratory

Experiment.9 Prelab

AC & DC Power Analysis and Design

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Supervised by:

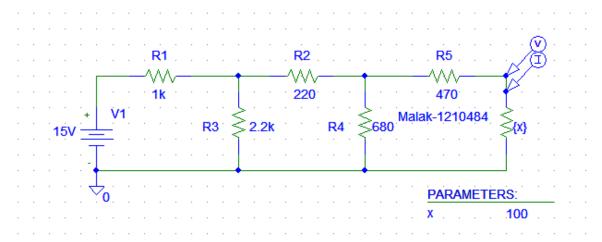
Dr. Jaser Sa'ed

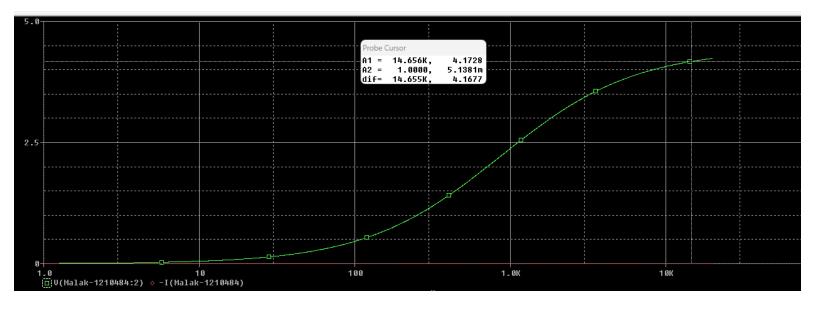
Teacher assistance:

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1-Make a DC sweep for RL in the circuit of Figure 9.5 to produce a plot of (PLOAD vs. RL) for the range of values of RL shown in Table 9.2, use cursors to find the value of RL that results in maximum power transfer to RL.



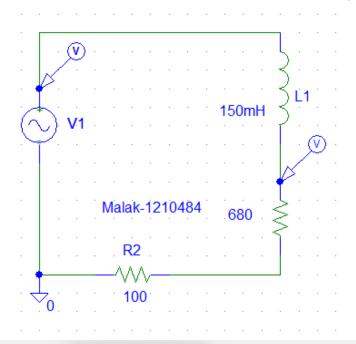


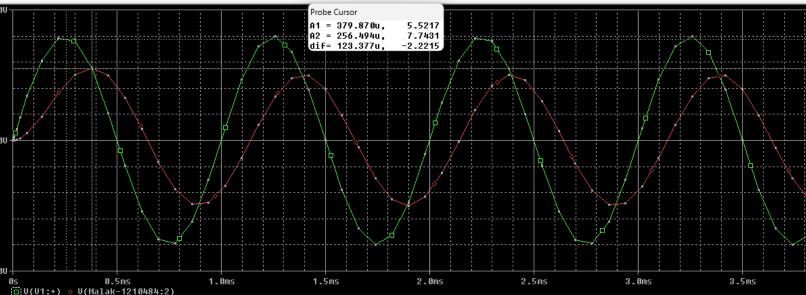
2. For the circuit of Figure 9.6:

- Use phasor analysis to calculate VL and IL, assume the input voltage Vin = 8 $VPP \angle 0^{\circ}$.
- Calculate the complex power of the load" SL.
- Calculate the parallel capacitance needed to correct the load power factor to unity.

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Malak Qadem
         → Exp.9:
        2) Find VL and IL when Vin = 8 Upp Loo
                                                                12/0484
        to Calculate IL = Vms & Vrms = Vin
        to Find Z = Ry+ R2 + jwl
           = 680 + 100 + j(2\pi(1000)(150\times 6^{-3}))
2\pi = 2\pi + j = 1223 + 150.4^{\circ}
        TL= Vin = 2.82 Lo => [TL=2.306x6-3 1-50.4 MA
       > to calculate & VL = ILZL
        VL = IL ZL
            = 2.306 x 10-3 L-50.4 x 10000 942 1+90
          i VL = 2.17 139.6
    · Complex power of the load SL
     SL = Vin TL" , TL = TL L-01 = 2.306×6-3 L 50.11
          = 2.82 10. * 2.306×6-3 1 50.4
         =6.5x6-3 1 50.4 = 0.00415+0.005
 · parallel capacitance C
  QC = SL. SinOs Same = 6.5 x 63 (Sin(So.4)) = QL= 0.005
\Rightarrow \mathcal{Q}C = \frac{V^2 rms}{WC} \Rightarrow XC = \frac{V^2 rms}{Q} \Rightarrow XC = \frac{(2.82)^2}{WC}
XC = \frac{1.0 \times 6^{-7}}{Q}
\Rightarrow C = 1.0 \times 6^{-7}
        2(3,14)(1000) €
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• Use PSPICE to do transient analysis of the circuit in Figure 9.6, show IL(t) and VL(t) on one graph, you will need different Y-axis, and measure the power factor (from the time difference between the two waveforms).



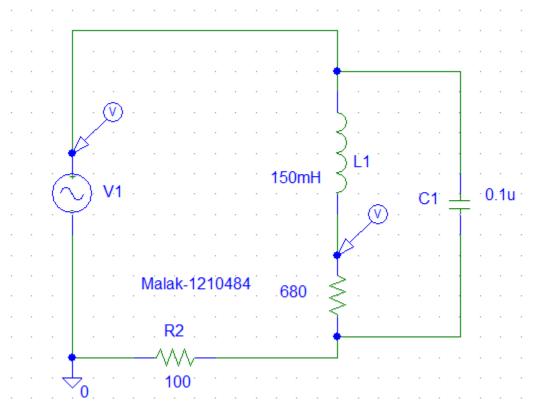


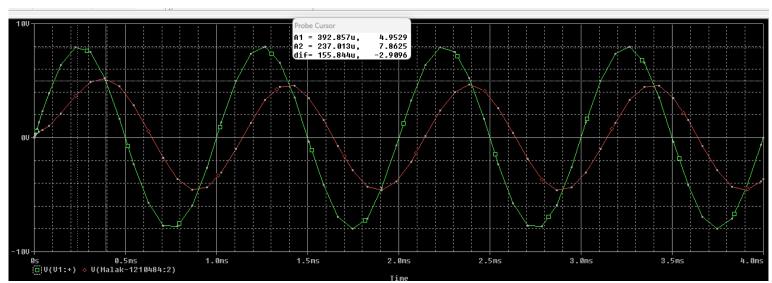
$$\Delta t = (379.870 - 256.494) \ \mu s = 123.377 \ \mu s$$

$$\theta = 360 * f * \Delta t = 360 * 1000 * 123.377 * 10^{-6} = 44.41^{\circ}$$

$$P.F = \cos \theta = \cos 44.41^{\circ} = 0.71$$

• Repeat the previous step with the added capacitor to show power factor improvement.



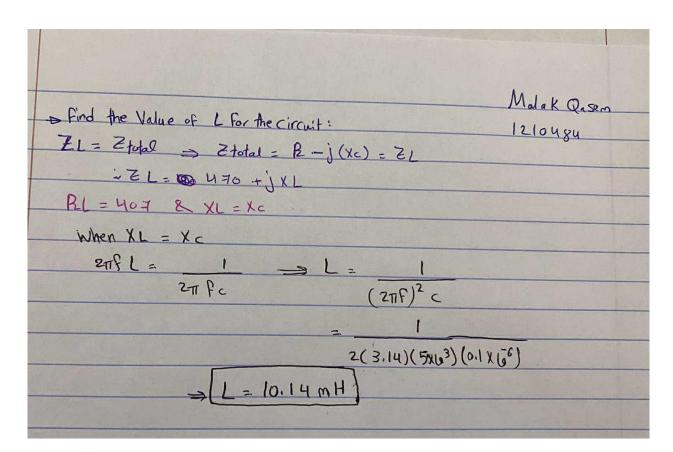


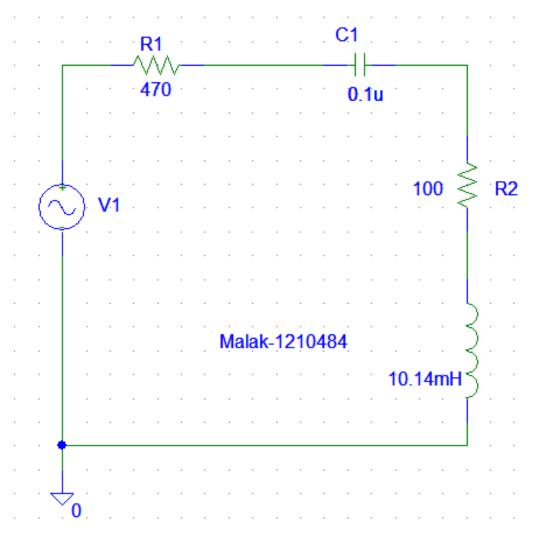
$$\Delta t = (392.857-237.013) \mu s = 155.844 \mu s$$

$$\theta = 360 * f * \Delta t = 360 * 1000 * 123.377 * 10^-6 = 56.10^{\circ}$$

$$P.F = \cos \theta = \cos 56.10^{\circ} = 0.56$$

3. For the circuit of Figure 9.8, design a load that is when connected to the output terminals of the circuit will extract maximum average power, then calculate magnitude of P Max.





• Find the magnitude of pmax:

Pmax =
$$(VH)^2$$

UPP = 10 \Rightarrow VHn = 7.07 V

R = 470 N

in Pmax = $(7.07)^2$
 $y(470)$

Pmax = 26.6 m W