

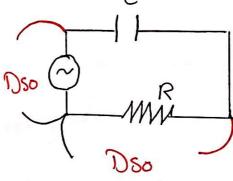
· Theoritical explanation: · low pass fullet only allows low frequency sugrials to puss · When the frequency is low the signed when it bravels through the output and when it is high it travels through the Capacitor. conat we use :-· R.C. . signal Generator & Dso و في و حل هذه الدارة • غيرهم التردر في المدى (٥٠٠ ـ ٥٠٠) وفس وزور لاهم Pro ceclure 101 de Voit rémèr A ups . · low pass filter: F-3dB JED , AVS f. (car). >. Change the frequety Dso in the Range (0.2--20/kHZ Dso and measure V > Calculate A by knowing that Vin = 10

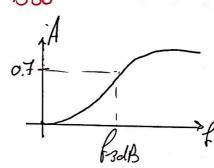
> Draw Ars of and optain bods of the Alaa Etaiwr

- high pass filler -> Connect this Cir Cuit

-> Same as the previouse

. Obtain frads





- · هُم رَجِلُ لَائِنَ (لَلْقَادِمَةُ مِلْ لَلُولِيِعِ)
 - م نفس خفوا التركب السابعه
 - folk devent.

Alaa Etaiwi