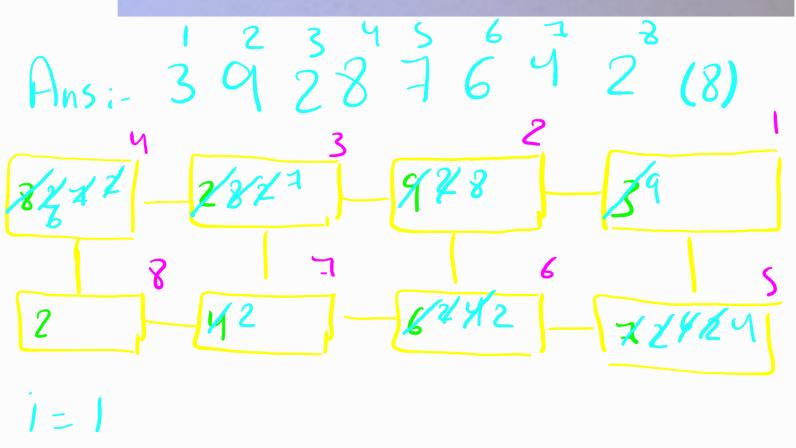


Question Five [25 marks]

Trace the below algorithm considering the given list of number. Please note that this algorithm is designed for a one-D (one dimensional) mish parallel processing architecture. Please show all steps.

```
The list of numbers:
3
                                           // n = 8
Procedure testCode(n)
Begin
        for i = 1 To n Do
        begin
                if i is odd then
                       for j=0 to n/2-1 do
                          if (A2j+1 < A2j+2)
                               exchange (A2j+1, A2j+2);
                 if i is even then
                        for j = 1 to n/2-1 do
                            if (A2j < A2j11)
                                exchange (A2j, A2j+1);
         end for
 end
```



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$$i=2$$

even $\rightarrow j=1 \rightarrow 3$
 $A_2 \subset A_3 = 2 \subset 8 \checkmark$
 $i=2$ $A_4 \subset A_5 = 2 \subset 7 \checkmark$
 $j=3$ $A_6 \subset A_7 = 6 \subset 4 \times$

$$i = 3$$

 $odd \rightarrow j = 0$ $A_{1} < A_{2} = 9 < 8 \times 1$
 $j = 1$ $A_{3} < A_{4} = 2 < 7 \times 1$
 $j = 2$ $A_{5} < A_{6} = 2 < 6 \times 1$
 $j = 3$ $A_{7} < A_{8} = 4 < 2 \times 1$

i = 4
even
$$j=1$$
 $A_{2}CA_{3} = 8C_{1}X$
 $j=2$ $A_{4}CA_{5} = 2C_{6}V$
 $j=3$ $A_{6}CA_{1} = 2C_{1}V$

$$i = 7$$

 $odd \quad i = 0 \quad A_1 < A_2 = 9 < 8 \times 1$
 $i = 1 \quad A_3 < A_4 = 7 < 6 \times 1$
 $i = 2 \quad A_5 < A_6 = 4 < 2 \times 1$
 $i = 3 \quad A_4 < A_8 = 2 < 2 \times 1$

$$i = 8$$

Even $U = 1$ $A_2 \subset A_3 = 8 \subset 7 \times 1$
 $j = 2$ $A_4 \subset A_5 = 6 \subset 4 \times 1$
 $j = 3$ $A_6 \subset A_7 = 2 \subset 2 \times 1$



Question Five [20 marks]

1) All computer architectures can be classified into four categories. Describe these categories in details?

2) What we mean by degree of parallelism?

3) Parallel processing topology is the way by which the processors in parallel computers are connected. Describe in details the Perfect Shuffle and Shuffle Exchange topology and mention the different between Perfect Shuffle and Shuffle Exchange.

Ans: -1) . SISD:-Single instruction Single Dater: -

4 One processor persons Single instruction at one et at any given hime Uploaded By: Haneen · MISD: - Multi instruction Single Date: -

There are multi Processors each perform disserent instruction but on the Same data Set.

. SIMD :-

There are multi processors

Perform the same instruction
but on different dates set.

· MIMD :-

There are multi processors

each persorm disserent

instruction and each one

Persorms on a disserent

data set.

2)

degree of parallelism:muter of processors
in a multiprocessor system

+ Perfect Shubble:each processor (i) is Connected to the Processor (i), His topology is used only if the number of processors is a power ot 2, we can determine the Connections between

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Processors by:

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 $\begin{cases} 2i & \text{if } 0 \leq i \leq \frac{n}{2} \\ 2i + 1 - n & \text{if } \frac{n}{2} \leq i \leq n \end{cases}$ or we can use the formula 2 i º/o(n-1) except-for the last processor which connects to itself. or by using the bit representation of the processor's indices, for each bit representation et the processois STUDENTS-HUB.com e Still Le Uploaded By: Haneen

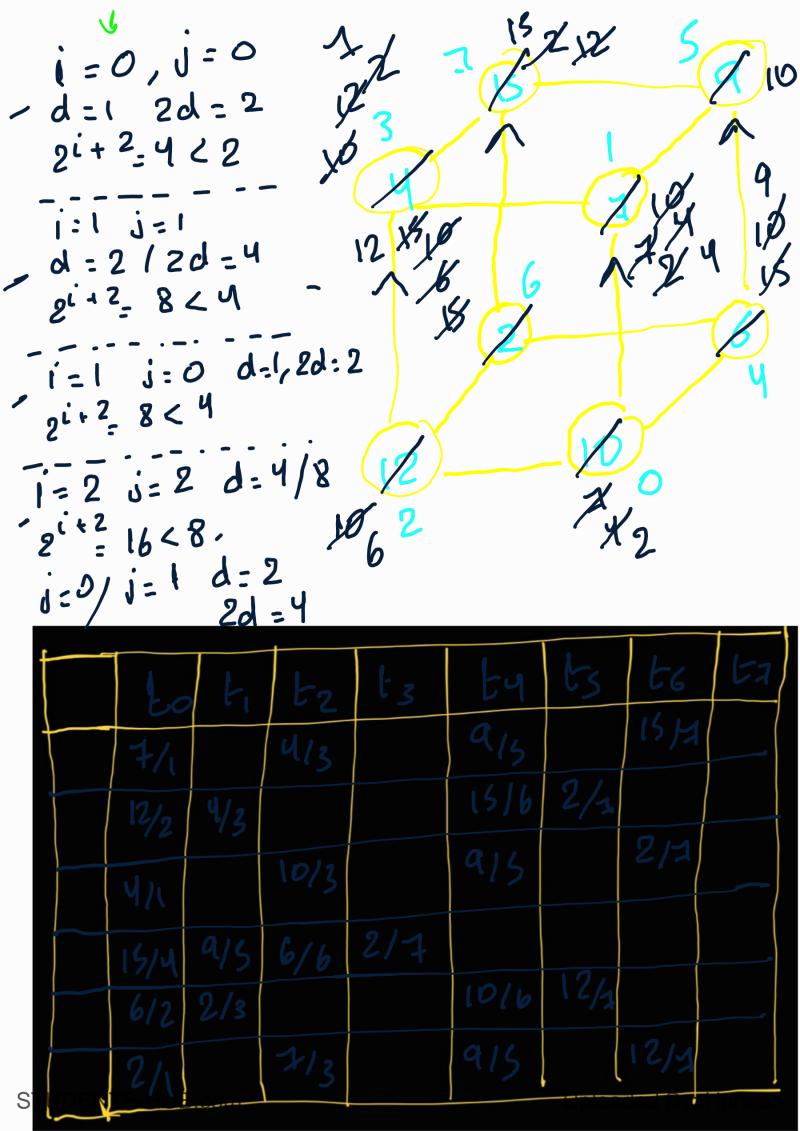
binary representation one position to the lest for example (001) connects with (010)

& Shelle - exchange: each odd processor Connects with the nexteven node and each next even node also Connects with previous odd node, in binary representation we can flip the last-bit

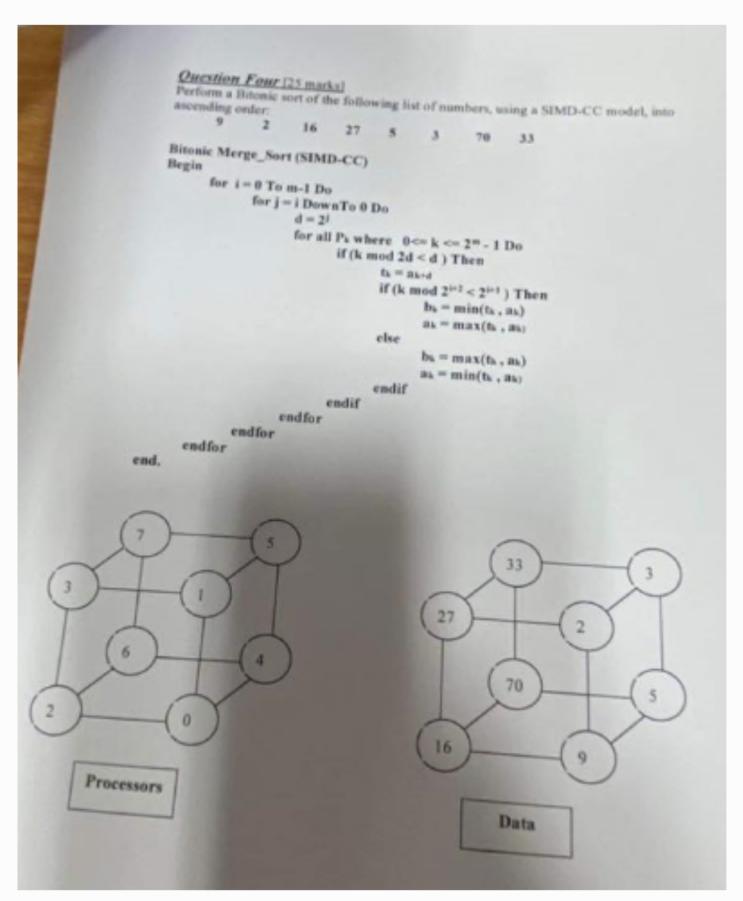
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A one disserent between then:the perfect shiftle is in one direction meaning if processor 3 connects with 6, 6 is not connected with 3, however in shible exchange if 1 Connects with 2, 2 also connects will 1, so it's 2 directional connection.







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