Problems with Bounded-Buffer with Counter

Concurrent access to shared data may result in data inconsistency.

Maintaining data consistency requires mechanisms to ensure the orderly execution

of cooperating processes.

- The statements:

1

- o counter = counter +1;
- counter = counter -1; 0

must be executed atomically.

Atomically: If one process is modifying counter the other process must wait, that is, as if this is executed sequentially.

atomically

to access a shared detta Concurrently

the shared data must be accessed

produção onsumer Counter--Counter++ Section

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The Critical Section Problem

The Problem with Concurrent Execution

- > (i.e.: Counter in preducer- consumer)
- Concurrent processes (or threads) often need access to shared data and shared resources.
- If there is no controlled access to shared data, it is possible to obtain an inconsistent view of this data.
- Maintaining data consistency requires mechanisms to ensure the orderly execution of cooperating processes.

<u>Race Condition</u>: A situation in where several processes access and manipulate data concurrently and the outcome of execution depends on the particular order in which the access takes place.

. n processes all competing to use some shared data

- Each process has a code segment, called <u>Critical section</u>, in which the shared data is accessed.

- Problem - ensure that when one process is executing in its critical section, no other process is allowed to execute in its critical section.

Structure of process repeat entry section | < critical section exit section | 🛹 remainder section until false;

Critical

ما العل عنز الخرول منذ اله اcritical critical section

R Shareel delta is Accessed Sherred Atomically data Ly one processat a time.

Solution Requirements:

Mutual Exclusion If process Pi is executing in its critical section, then no other processes can be executing in their critical sections. "One process at a time

Progress. If no process is executing in its critical section and there exist some processes that wish to enter their critical section, then the selection of the processes that will enter the critical section next cannot be postponed indefinitely.

IT I there are no process in the critical section and pooss wants to use the Bounded Waiting A bound must exist on the number of times that other processes Critical Section are allowed to enter their critical sections after a process has made a request to enter it can get its critical section and before that request is granted.

- Hich section band for each polass is granted. Hime it needs to get the Critical Section Assume that each process executes at a nonzero speed.
- حث لا دُحرز الا الم contrict المنافية ، تمين تقييم الصع No assumption concerning relative speed of the n processes. ->.

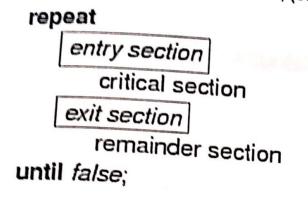
Solution to Critical Section Problem

Types of Solutions

- Software solutions programming
 - Algorithms whose correctness does not rely on any assumptions other than positive processing speed (that may mean no failure).
 - o Busy waiting.
- Hardware solutions
 - Rely on some special machine instructions. 0 Usystem calls
- Operating system solutions Ready functions to support the programmer
 - Extending hardware solutions to provide some functions and data structure 0 support to the programmer.

SOFTWARE SOLUTION

- Only 2 processes, P₀ and P₁
- General structure of process P_i (other process P_j)



Processes may share some common variables to synchronize their actions.



Shared variables: -

int turn; //turn can have a value of either 0 or 1 //if turn = i, P(i) can enter it's critical section > works based on turns Process P_i do Process Pi while (tu COB critical section while(turn1=1) turn = j; do nothing i remainder section critical section i } while (true) turn= lo reminder Section

- Mutual exclusion (ok)

- Bounded waiting (k) - each only waits at most 1 go.

<u>Progress not good</u> each has to wait 1 go. P_0 gone into its (long) remainder, P_1 executes critical and finishes its (short) remainder long before P_0 , but still has to wait for P_0 to finish and do critical before it can again.

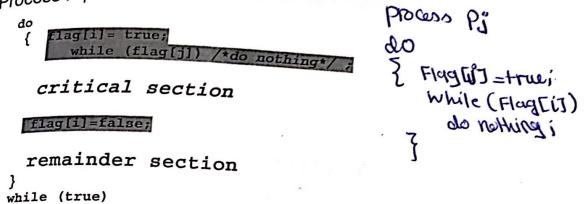
Strict alternation not necessarily good - Buffer is actually pointless, since never used! Only ever use 1 space of it.



Shared variables

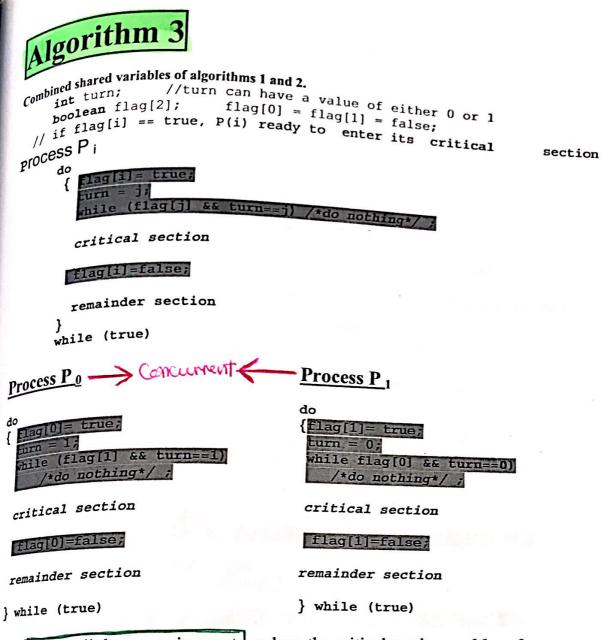
```
boolean flag[2];
flag[0] = flag[1] = false;
// if flag[i] == true, P(i) ready to enter its critical
section
```

Process P i



• Doesn't work at all, Both flags set to true at start. "After you." "No, after you." "I insist." etc.

Infinite loop



- Meets all three requirements; solves the critical section problem for two processes.
- "flag" maintains a truth about the world that I am at start/end of critical. "turn" is not *actually* whose turn it is. It is just a variable for solving conflict if two processes are ready to go into critical. They all give up their turns so that one will win and go ahead.
- e.g. flags both true, turn=1, turn=0 lasts, P₀ runs into critical, P₁ waits. Eventually P₀ finishes critical, flag =false, P₁ now runs critical, even though turn is still 0. Doesn't matter what turn is, each can run critical so long as other flag is false. Can run at different speeds.
- If other flag is true, then other one is either *in* critical (in which case it will exit, you wait until then) or at start of critical (in which case, you both resolve conflict with turn).

Bakery Algorithm - generalization of the solution Introduction Introlline to the critical section problem for *n* processes in software. The basic idea is that of This algorithm solves take numbers, and whoever has the lowest number gots contained is that of the every customers take numbers and whoever has the lowest number gots contained in the taken of the every customers and the number of the every customers are contained in the every customers are customers and whoever has the lowest number gots are customers and the every customers are customers and the every customers are customers and the every customers are customers are customers are customers and the every customers are customers are customers are customers are customers and the every customers are customers and whoever has the lowest number gots are customers This algorithm solved the ormetal doubled problem for n processes in software. The basic idea is that the basic idea is that a bakery; customers take numbers, and whoever has the lowest number gets service next. Here, of a bakery "service" means entry to the critical section. a bakery, service" means entry to the critical section, course, "service" means entry to the critical section, Critical section for n processes Generalization for n processes. Each process has an id. Ids are ordered, Each process rate and an an an an area of the section, process receives a number. Holder of the smallest number enters the children in the same number, if i < j, then P₁ is served first; else P₁ is if j < j, then P₁ is served first; else P₁ is The numbering scheme always generates numbers in increasing order of enumeration; i.e., Notation <= lexicographical order (ticket #, process id #) o (a,b) < (c,d) if a < c or if a = c and b < d $\max(a_0, \ldots, a_{n-1})$ is a number, k, such that $k \ge a_i$ for $i = 0, \ldots, n-1$ Shared data boolean choosing[n]; //initialise all to false int number[n]; //initialise all to 0 2 do 3 { choosing[i] = true; 4 number[i] = max(number[0], number[1], ..., number[n-1]) + 1; choosing[i] = false; for(int j = 0; j < n; j++) { while (choosing[j] == true) 8 /*do nothing*/ 9 while ((number[j]!=0) && (number[j],j) < (number[i],i)) 10 /*do nothing*/ 11

13 critical section

14 number[i] = 0;

15 remainder section

} while (true)

Comments

12

lines 1-2: Here, *choosing[i*] is true if P_i is choosing a number. The number that P_i will use to enter the critical section is in *number[i*]; it is 0 if P_i is not trying to enter its critical section.

lines 4-6: These three lines first indicate that the process is choosing a number (line 4), then try to assign a unique number to the process P_i (line 5); however, that does not always happen. Afterwards, P_i indicates it is done (line 6).

lines 7-12: Now we select which process goes into the critical section. Pi waits until lines 7-12. Note number of all the processes yoes into the critical section. Pi waits until the lowest number of all the processes waiting to enter the critical section. If an arocesses have the same number, the one with the smaller processes have the same number. it has the lowest the same number, the one with the smaller name - the value of w^{0} processes have the notation "(a,b) < (c,d)" means true if a two processes in the notation "(a,b) < (c,d)" means true if a < c or if both a = c the value of the subscript - d (lines 9-10). Note that if a process is not trying to option the subscript - d (lines 9-10). the subscript of government $(a,b) < (c,a)^n$ means true if a < c or if both a = c and b < d (lines 9-10). Note that if a process is not trying to enter the critical section, and b < d (lines of the process is not trying to enter the critical section, its number is 0. Also, if a process is choosing a number when P_i tries to look at it, P_i waits until it has done so before looking (line 8).

line 14: Now P_i is no longer interested in entering its critical section, so it sets number[i] to 0.

Drawbacks of Software Solutions

- Complicated to program .
- Busy waiting (wasted CPU cycles .
- It would be more efficient to block processes that are waiting (just as if they had requested I/O).

HARDWARE SOLUTION

Hardware Solution Disable Interrupts

- Hardward Stranger and Stranger You can only afford to do this for a little while, so you don't lose any interrupts (of course in general you don't want to protect expensive things with spin locks) general you don't want to protect expensive things with spin locks).
- general you down with a device you sure can't use a spin lock! (DEADLOCK).
- Correct solution for a uni-processor machine, but this doesn't work on multiprocessors, the
- solution is not control of a solution of the s

Repeat disable interrupts critical section enable interrupts remainder section Forever

Hardware Solution Test and Set ~ must be executed Atomically Use better (more powerful) atomic operations: Test and modify the content of a word atomically.

of target.

boolean Test_and_Set (Boolean & target) Call by reference to return the value

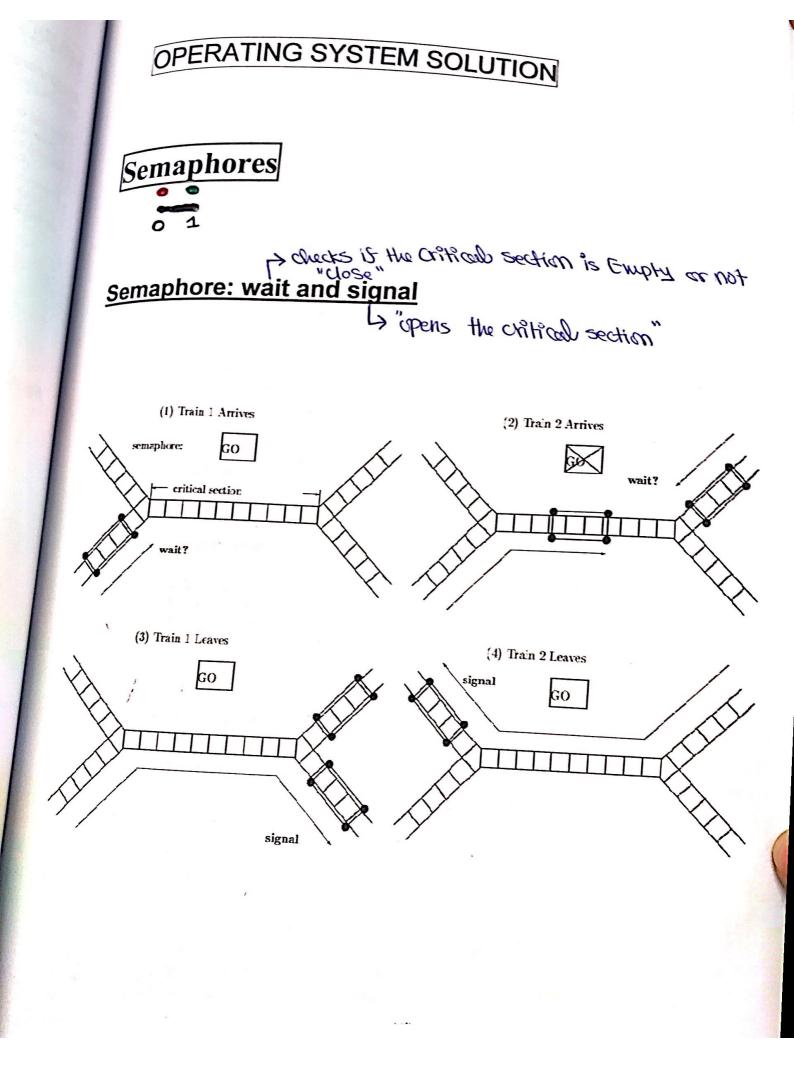
```
target = true;
return test;
}
```

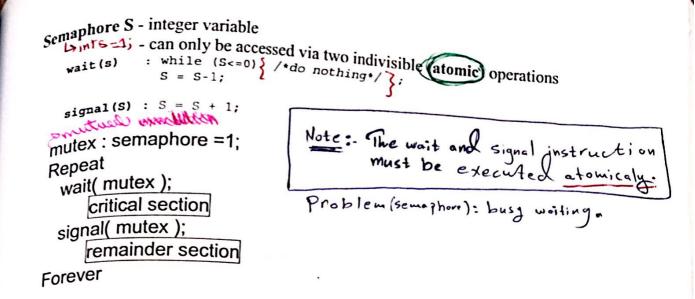
Shared data: boolean lock = false;

Process P_i

do { while (Test-and-Set(lock)) /*do nothing*/ ; critical section lock = false; remainder section }while (true)

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Semaphore Implementation

Define a semaphore as a record/structure struct semaphore { int value; List *L; //a list of processes } P-P-

- Assume two simple operations:
 - block suspends the process that invokes it.
 - wakeup(P) resumes the execution of a blocked process P.
- Semaphore operations now defined as

```
wait(S)
{ S.value = S.value -1;
    if (S.value <0)
        { add this process to S.L;
        block;
     }
}
signal(S)
{ S.value = S.value + 1;
    if (S.value <= 0)
        { remove a process P from S.L;
        wakeup(P);
        }
}</pre>
```

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PI

Classical Problems of Synchronization

- Bounded Buffer Problem
- Readers and Writers Problem
- Dining Philosophers Problem

```
Bounded Buffer Problem
```

Shared data

```
char item; // could be any data type
char buffer[n];
semaphore full = 0; // counting semaphore
semaphore empty = n; // counting semaphore
semaphore mutex = 1; // binary semaphore
char nextp, nextc; mutual execution,
```

```
Producer process
```

```
do
{ produce an item in nextp
  wait (empty); _____ Ohecks if Buffers's full
  wait (mutex); ______ Counter
    add nextp to buffer
    signal (mutex);
    signal (full);
}
while (true)
```

```
    Consumer process
```

```
do
{ wait( full );
   wait( mutex );
   Iremove an item from buffer to nextd
   signal( mutex );
   signal( empty );
   consume the item in nextc;
}
```

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Readers-Writers Problem

- Shared data
 - semaphore mutex = 1;
 semaphore wrt = 1;
 int readcount = 0;
- Writer process
 - wait(wrt);
 writing is performed
 signal (wrt);
- Reader process

wait (mutex); readcount = readcount + 1; if (readcount ==1) wait (wrt); signal (mutex); reading is performed wait(mutex); readcount = readcount - 1; if (readcount == 0) signal (wrt); signal (mutex);

Dining Philosopher Problem , Shared data semaphore chopstick[5]; chopstick[] = 1; aucilable Philosopher *i*: . { wait (chopstick[i]); wait (chopstick[i+1 mod 5]); eat; signal (chopstick [L]); signal (chopstick [i+1 mod 5]); think; SI , while (true) So Pa 1: available 0 philosopher P. Sa 1 stick Pa 53 1. Problems: (1) Dead lock. (2) Starvation.

Chapter #7 DeadLocks 28 Definition: Two processes are dead locked, if every process is holding a resource of waiting for the other process to release throwsame. I and impla TONY. Printer is allocated and PR-1 to the pieces (B) Caycle Diterilar Et contar Pa

monday April 16:2018 energialis head langer [] Dead lock: H set of waiting (blocked processes) 1 each process 4 is helding a resamce of waiting for other processes-to release its resources 6 -5 PR-1 5 antia (cycle) 5 3 27 dA (8 Mt Contan ... At System models to pole in many proper printers we have the resource types Ro, R. (-1), RA-1 he have Wi instances of each resource type 1 to Wohn with and 19 93 Earch process use the resamos in the following orders * Requests the resaurces enoulog at the resources. * Releases the resalles. anot rig Bt withour signal [] Deadlock handling: appressi >11 The OS handles the dead lock in one of two methods; (1) Allow the system to enter a deadlock and then recovers from it. "UNIX" (2) The OS prevents the system from enterinal a deadlack

55 Et Necessary Conditions: 4 necessary conditions must hold simulatanously Linin order for a deadlock to accur. The H T (1) Mutual Exclusioning painted of The resource type must be used exclusively that's count be sharred "for more them one person diatime" (2) Had & weil: Each process is holding a resulter type and waiting for the other process to release the rescarce of the same type. (3) No Pre-emption: Cont remove any of the resources. (4) Circular wait : " cycle H word on gut one on There exists a sequence of processes < Po, P1, P2, 10, Pn-1> Such that's privated and Pois waiting for Py to release its 10 110 hesallos Paris waiting for P2 to release its min sintesairconanoral in Pn-2 is waiting for Pn-1 to release its resamos. pailleyon tollow allo NI Potchoo Par Porcal - Pr-22 Phil orthopp our Anthone a chart of mul Caycles another 1914 locus real " ti orest (9) The OS prevents the system from entering a dear . starts

Et Resource Allocation Graph: Generally, a graph G= (V=E) F V= set of vertices. = Set of edges. 1 In the deadlock Case: 1 V- P: pocess. **-**1 R: resource type. A (Pij Pj): Process Pi is requesting one instance 4 of resource type Risso (Rj. Pi); one instance of resource type Rj hard find is allocated or given to process Pi Manubias Januar Examplettup= { Pro Pro Profe, March pa R-J R1(1), R2(2), R3(1), R4(3) $E = \frac{5(P_1, R_1)}{(P_2, R_3)} (R_1, P_2) (R_2, P_2) (R_2, P_1) (R_2, P_2) (R_2, P_1)$ (R3, P3) 2 Mary 2 Mark W ab. HADARD) R3 R1 P2 Pz Ra Ry Assume B demands an instance of R3.

6 SEOJ 6 P4 20 SC4] SEI] (P3 P1 SE2 SEZ] It Deadlock prevention:-To make sure at least one of the four necessary conditions don't hold. 1_ mutual exclusion. By default, some resamces are nuttually Print pro do Fries and we can't do any thing about it such as printers. -(R. R) Stow & chloth To break this condition we might do. (I) Let the process request all its rescorces at the beginning. (II) The process is granted all its resources when it has none Problem; Starvation Smotoni no skrangs & sourcell

-3_ Non pre-emption: 1 millight to daget on ~ IF a process requests a resource which is not available, it must release the rescarces it hers. Problem: low system utilization. poor performence, + 3 4 in addition to starvation. 4_ Chrcular wait: (1) (2) Hard disk of the server (3) Tape. dia 7 2 De Printer 16 And Mar of 9. 9 und borbelog 20 miler 18 2 Process Pis dant & student Semaphor int s [i] = 21,1,1,1,1,1, Repeat I yet los sales as were lovo Think i advinit & struggy wait (S?) \rightarrow wait (Smin(i, ((i+1)) (.5))) wait (5 ((i+) %5)) . Juait (Smax (i ((i+)) (5))) Eat ; 8 love Signal (S((i+1)%5)) juiting H alguns motion Signal (Sidsiane AL 200 psig Funtil False. i odde dod Provid more a doctorallin 26004 Dur man 8 10 5 6 11 9 <u>6</u> 0 9

[] Deadlerk Avoidance; Destinition: A system is in a safe state, if there exists a sequence of processes < Por Pir P21 --- Ph-1> such Huelti Po can take all aveilable resources, SJexecute & Finish. S.F Pi can take all anoulable resauces, 57 & resources released by Por execute 67 & Finish. Solution (2) 67 P2 cour take all available resources, -& resurres released by PorPor 2 execute of Finish. Pn-1 cour take all curaitable resames 1 and resames released by E.P. P. P. P. -21 E execute & Finish. I doint P Definition; If there's such a sequence, then - the system is safe, NO dealed lock. example: A system with 12 tape units and 3 processes, A snapshot at the system looks like i salit 1000 Process -mox needs allocateal current needs 2 10 5 5 P. 4 ... 2 ... 0 α 23 6

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the available at this time: 13 +12-5 allocated(9) Is the system safe. TR available: 3-26 <P2, Po, P2> THE 50(10) TRO 10×12 Safet 120 Assume, pores 2 demended extra type & the CS granted the request. is the system safe? 12 -29 the quallable at this time; -03 available: 24 <P1, 220 anisod sitt -----demon 2010297 0 there (X) No safe Sequence deadlack -69 Process Allocation to the maxing all current needs. 19 A BONCH STIMP AVIBUCH 2001 AF B 10 10 Jon 7153 1 74 3 to 0 Pa 19 200 322212122 Pa B OBSA 11.3 TO 2000 19-10 2011 6 0 Para att 12 Daw 1/2 18 1000 2 2 2 2 - \bigcirc 3000 403310 43 -IS the system safe PIIs there a safe sequence PI white Kather Available A CICB Cr total 332 tian 1 200 \$ 3 2000- 10, 5 7 NF 4 36 (P1, P3, P0, Pg, P4) THE FIFT SCUA 10 5 5 10 5