



To  
**ENEE236**  
**Analog Electronics**

**Instructor**  
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**Main Reference1 (~ text book) :** Electronic Devices and Circuit Theory  
, 10<sup>th</sup> Edition **by** R. Boylestad & L. Nashelsky

**Main Reference 2:** Electronic Devices, 8<sup>th</sup> edition, by Floyd

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## **Course Objectives**

- Study diode construction, basic operating principles and modeling.
- To analyze and design diode based circuits used in different applications such as ac-dc rectifiers, limiting and clamping, voltage multiplication.
- To Study zener diode operation and usage as voltage regulator.
- To Study construction, operation, biasing of Bipolar Junction Transistors and Field Effect Transistors.
- To design and analyze BJT and FET based amplifier circuits using small signal analysis techniques including their high and low frequency response
- To study operational amplifiers and how to use them in various applications such as amplification, summation, comparison, integration, differentiation
- To study different discrete and integrated circuit Voltage Regulators and be able to design them for different applications

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Course Contents	
1.	Introduction to Semiconductors and Semiconductor diodes <ul style="list-style-type: none"><li>• Atomic Structure; Semiconductors , Conductors And Insulators; Covalent Bonds;</li><li>• Conduction in Semiconductors; N-Type and P-Type Semiconductors</li><li>• The diode; biasing a Diode; V-I Characteristics of a Diode; Diode Models</li></ul>
2.	Diode Applications <ul style="list-style-type: none"><li>• Load Line Analysis, Half-Wave and Full-Wave Rectifiers; Power supply Filters and Regulators; Diode Limiting and Clamping Circuits; Voltage Multipliers; The diode Data Sheet, Zener Diodes and their Applications</li></ul>
3.	Bipolar Junction Transistors (BJT) <ul style="list-style-type: none"><li>• Transistor construction and operation, Transistor Characteristics and Parameters; The Transistor as an Amplifier; The Transistor as a Switch.</li></ul>
4.	DC Biasing of BJTs The DC Operating Point (Quiescent Operating Point); Voltage-Divider Bias; Other Bias Methods

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Course Contents	
5.	BJT AC Analysis Amplifiers and small signal analysis, Transistor AC Equivalent Circuits- Hybrid Parameters, Common-Emitter Amplifier; Common-Collector Amplifier; Common-Base Amplifier; Multistage Amplifiers.
6.	Field-Effect Transistors (FETs) The JFET; JFET Characteristics and Parameters; JFET Biasing; The MOSFET Characteristics and Parameters; MOSFET Biasing
7.	FET Amplifiers. FET Amplification; Common-Source Amplifiers; Common- Drain Amplifiers and Common-Gate Amplifiers;
8.	Operational Amplifiers and Applications Introduction to Operational Amplifiers; Op-Amp Input Modes and Parameters Negative Feedback; Op-Amps with Negative Feedback ; Comparators; Summing Amplifiers; Integrators and Differentiators. Instrumentation Amplifier; Converters and Other Op-Amp Circuits.

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Course Contents	
9. Amplifier Frequency Response	
Basic Concepts; The Decibel; Low-Frequency Amplifier Response.	
High- Frequency Amplifier Response; Total Amplifier Frequency Response.	
10. Voltage Regulators	
Voltage Regulation; Basic Series Regulator; Basic shunt Regulator; Integrated Circuit Voltage Regulators.	
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Grading Policy	
Quizzes	15%
Projects	15%
Midterm Exam	30%
Final Exam	40%
7	

# Introduction to Semiconductors and Semiconductor Diodes

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## Electronics Circuits

- We encounter electronics in our daily life in form of telephones, radios, television, audio equipment, home appliances, computer and equipment for industrial control and automation .

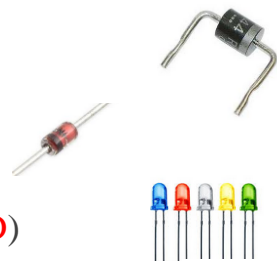


- The field of electronics deals with the design and application of electronic design .

# Electronics Devices

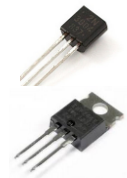
## ► Diodes

- a) Rectifier diode
- b) Zener diode
- c) **L**ight **E**mitting **D**iode (**LED**)



## ► Transistors

- a) **B**ipolar **J**unction **T**ransistor (**BJT**)
- b) **F**ield **E**ffect **T**ransistor (**FET**)



## ► Integrated Circuits (IC)

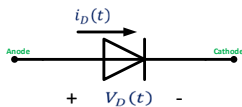
- a) **O**perational **A**mplifier (OpAmps)
- b) Voltage Regulators



# Diode

- It is an electronic device with a single **p-n** junction and it has the ability to conduct current in one direction while blocking current in the other direction.

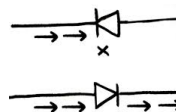
## ► Circuit Symbol :



## Example Component



- Physical construction

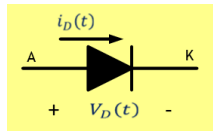


# Diode

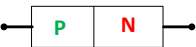


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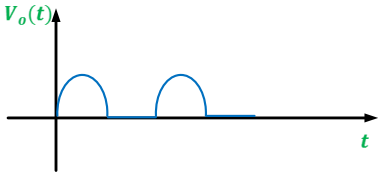
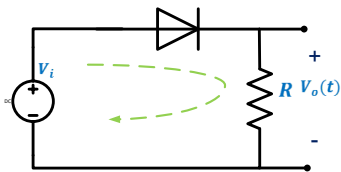
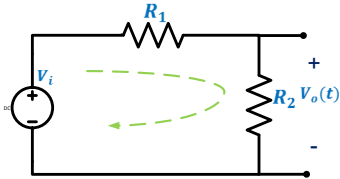
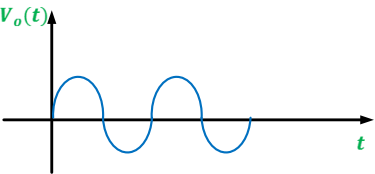
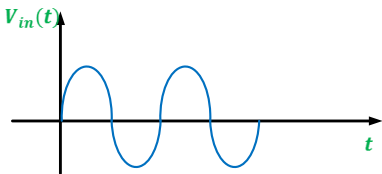
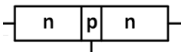
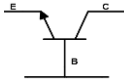
► Circuit Symbol :



- Physical construction



## Physical construction of the npn BJT



# Semiconductors

- ▶ Electronic devices as diodes, transistors and integrated circuits are made of semiconductor material .
- Semiconductors : materials whose resistance lies between **low** resistance of **conductor** and the high resistance of **insulator** .

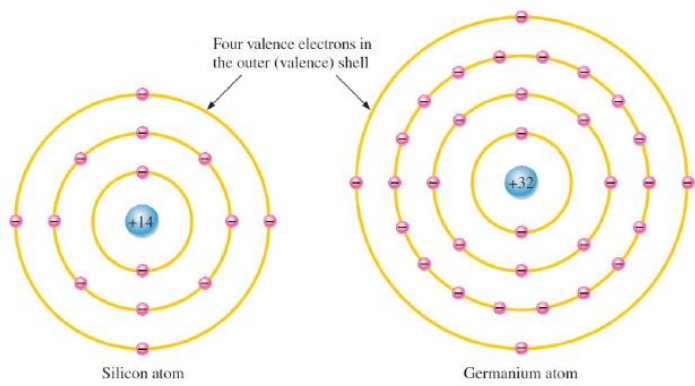
**Materials can be classified by their ability to conduct electricity.**

**1 - Conductors : Materials that easily conduct electrical current**

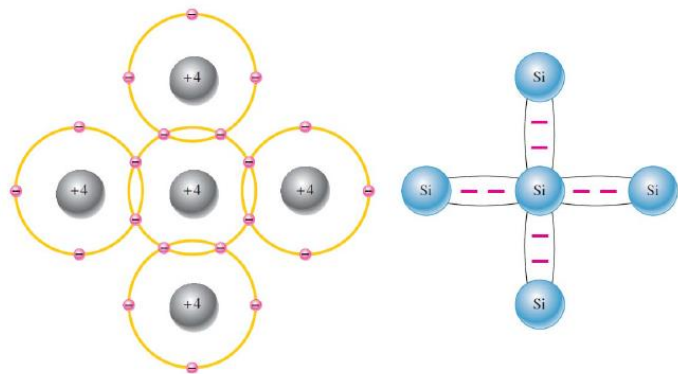
**2 – Insulators : Materials that do not conduct electrical current under normal condition**

**3 – Semiconductors: Material that are between conductors and insulators in their ability to conduct electrical current**

### Atomic structure



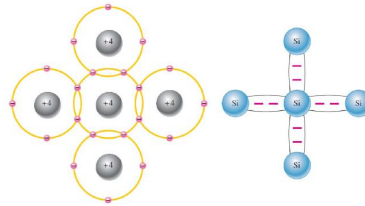
### Covalent bond



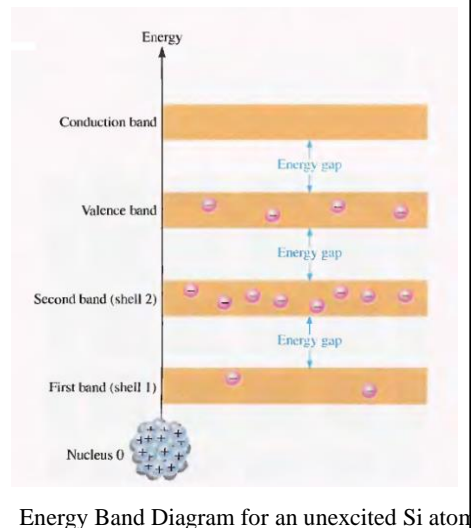
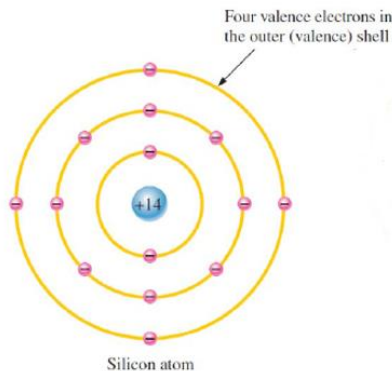


## Covalent bond

- ▶ A silicon (Si) atom with its four valence electrons shares an electron with each of its four neighbors
- ▶ This effectively creates eight shared valence electrons for each atom and produces a state of chemical stability .
- Also, this sharing of valence electrons produce the covalent bonds that hold the atom together; each valence electron is attracted equally by the two adjacent atoms which share it .

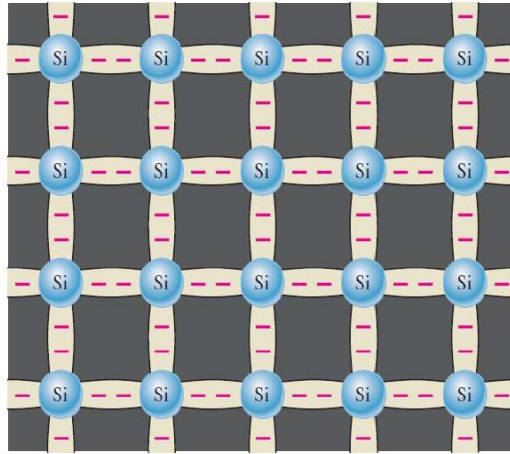


## Conduction in Semiconductors



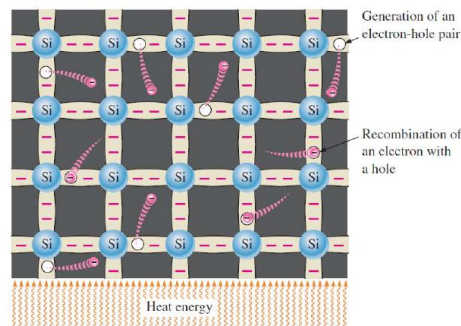
## Covalent bond in silicon crystals

- At absolute zero degree ( $-273\text{ }^{\circ}\text{C}$ ) all valence electrons are tightly bonded to their atoms and there is no free electrons, so the silicon behave as an insulator .



## Rupture of the a covalent bond

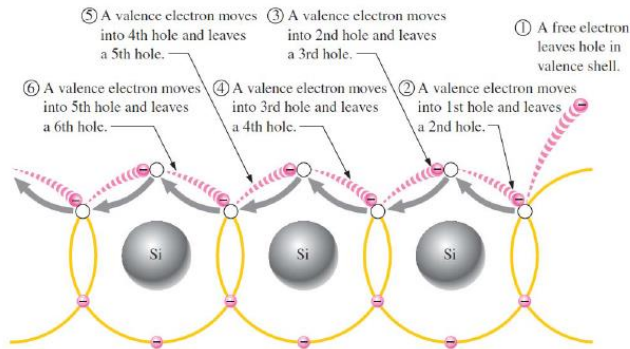
- When an electron becomes free that is unattached to any atom, a vacancy is left in the valence band within the crystal . This vacancy is called **hole** .
- For every free **electron**, there is one **hole** .
- One broken covalent bond  $\rightarrow$  one free **electron** + one **hole**
- At room temperature there is one broken covalent bond for every  $3 \times 10^{12}$  pure Si atoms .



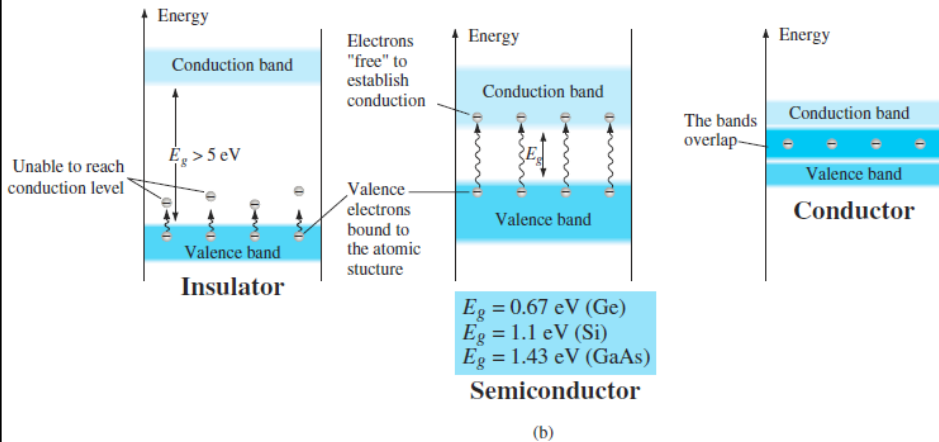
- At room temperature there are few available charge carriers (free **electrons** + **holes**)

# Hole motion

- When a valence **electron** moves left to right to fill a **hole** while leaving another **hole** behind, the hole has effectively moved from right to left.



## Energy Gap



## Doping

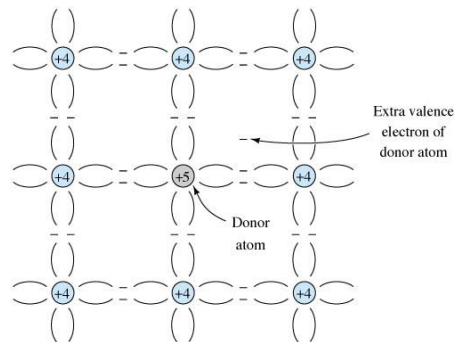
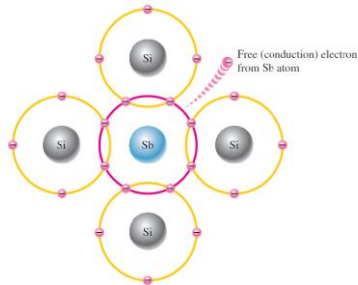
- A manufacturing process that adds free charge carriers (free **electron** or **hole**) into a pure semiconductor material to increase its conductivity
- There is two categories of impurities: n-type or p-type

### N-Type Semiconductor

- Pentavalent impurity ( one which has 5 valence electrons) atom is added such as phosphorus
- This atom forms covalent bonds with 4 adjacent silicon atoms, while the fifth becomes a conduction electron since it is not attached to any atom

## n-type silicon

n-type silicon is created by adding valence five impurity atoms.

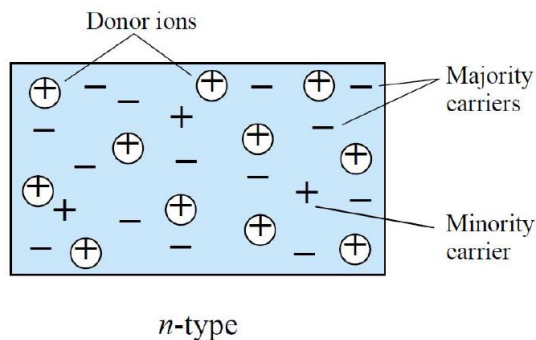


With the number of electrons increasing, the rate of recombination between free electrons and holes also increase

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- Number of conduction electrons can be carefully controlled by the number of impurities added
- Since most of the current carriers are electrons, this type of material doped with pentavalent impurities is an n-type semiconductor
- The majority current carriers in n-type material is electrons, but there are few holes created when electron-hole pair are thermally generated, these holes are minority carriers

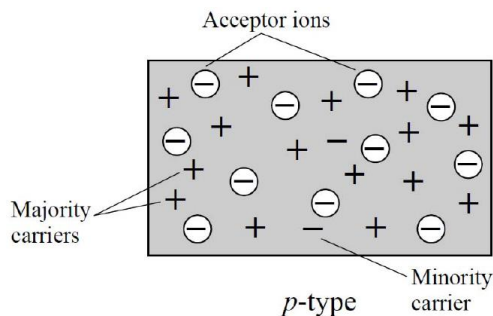
In the **n**-type material the free **electrons** are the **majority** and the **holes** are the **minority**.



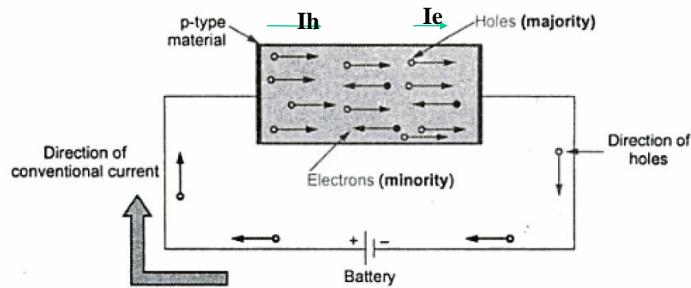
## P-Type Semiconductor

- To increase number of holes in intrinsic silicon, trivalent impurity atoms are added (atoms with three valence electrons) such as boron (B) or gallium (Ga)
- Valence electrons (3) of the impurity atom create covalent bonds with three adjacent atoms of silicon and a fourth electron is missing, creating a hole with each added impurity atom
- Majority carriers in P-type material are holes
- Also there are few free electrons that are created when electron-hole pair are thermally generated, these electrons are minority carriers

In the **p**-type material the **holes** are the **majority** and the free **electrons** are the **minority**.

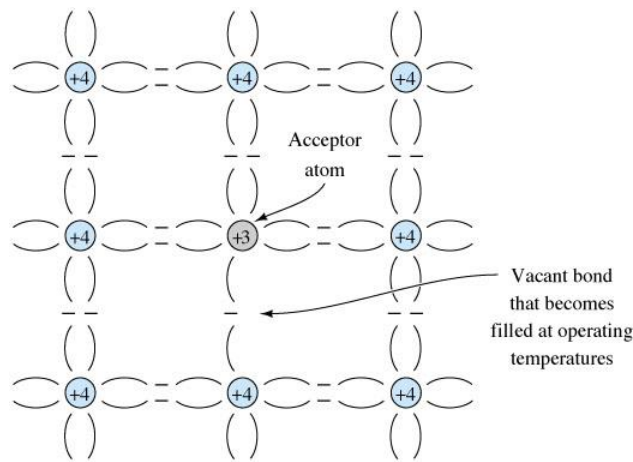


# Conduction in p-type material



Conduction in p-type material

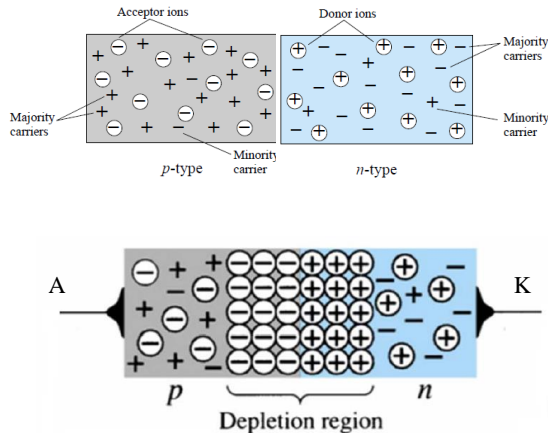
## p-type silicon



p-type silicon is created by adding valence three impurity atoms.

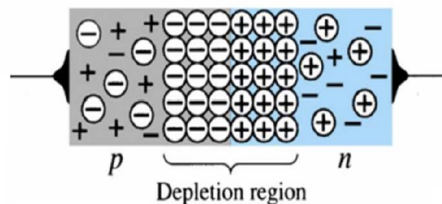
# Pn junction

The **p-n** junction is the basis for diodes, certain transistors ,and other devices.



## Formation of Depletion Region

- 1 ) **Electrons** from the **n**-type material near the junction diffuse across the junction.
- 2) These **electrons** fill the **holes** in the **p**-type material adjacent to the junction.
- 3) As a result of **electrons** leaving the **n**-type material , **donor ions are created** on the n side of the junction .
- 4) When these **electrons** fill holes in the **p** side of the junction , **accepter ions** are produced.
- 5) A wall of stationary **positive** ions is aligned with a wall of **negative** ions along the **n** and **p** sides of the junction .

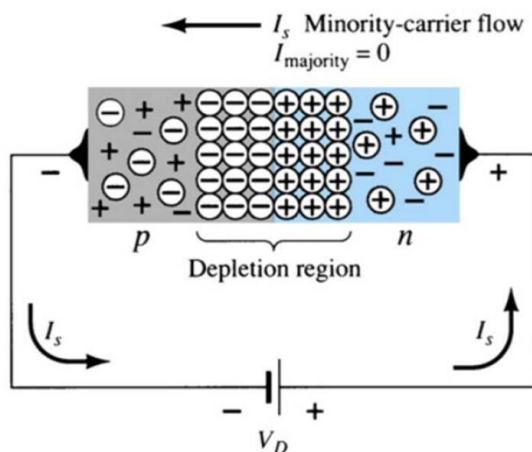




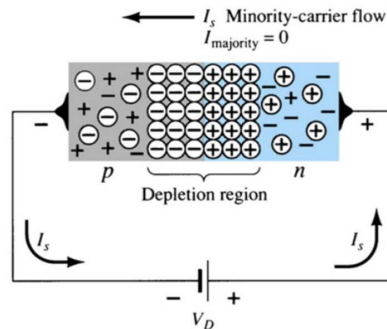
- 6 ) The space occupied between the ion walls is called **depletion region**.
- 7 ) Whenever there exists a **positive** charge with respect to a **negative** charge , a voltage difference is set between charges ;(**Junction potential**, **Junction barrier**).
- 8 ) The **junction potential** acts as **potential barrier** that tend to prevent majority carriers from crossing the junction.
- 9 ) Minority carriers are aided by the **junction potential** .

## Reverse bias of a pn junction

***Reverse bias is when the voltage at the n-side is higher than p-side***

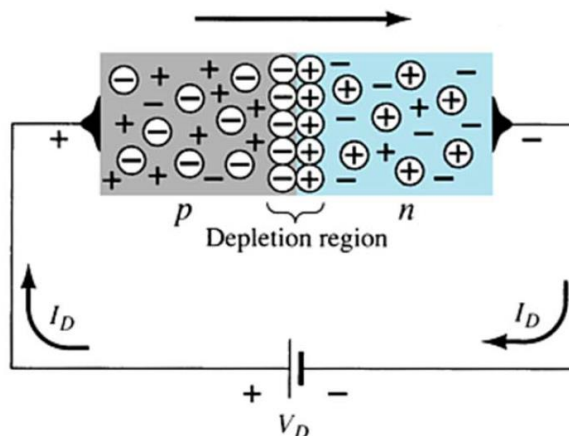


- The reverse voltage causes the depletion region to **widen** .
- The **electrons** in the **n**-type material are attracted toward the **positive** terminal of the voltage source .
- The **holes** in the **p**-type material are attracted toward the **negative** terminal of the voltage source .

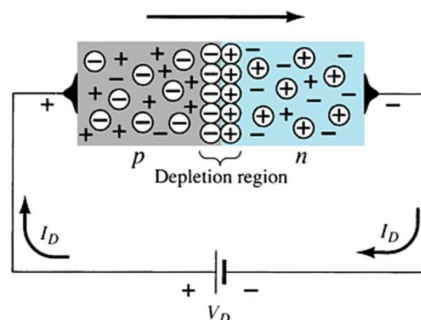


## ***Forward bias of a pn junction***

***Forward bias is when the voltage at the p-side is higher than n-side***



- ▶ The forward voltage causes the depletion region to **narrow**
- ▶ The **electrons** and **holes** are pushed toward the **p-n** junction
- ▶ The **electrons** and **holes** have sufficient energy to cross the **p-n** junction



<https://www.youtube.com/watch?v=OsfguONJw2Q>

[https://www.youtube.com/watch?v=ar7xDMR4P\\_U](https://www.youtube.com/watch?v=ar7xDMR4P_U)