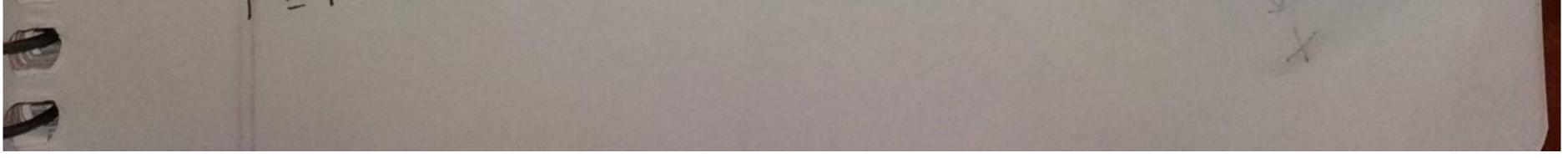
Chapter 3 - Gate level Minimization -And and C - How to make cost Reduction for any digital circit. T - The complexity of digital logic gates is directly proportional to the boolen expression from which the Function implemented Boolean F = X. 4 + X. 4 F Algebra'. F = X (4+9) More Simplify F = X. X. + ch2. juilione & Minimize the Function Using algebra is a Wakword approach g So we need another way for minimization. of Map Method for Minimization. alles Note: - Truth table is unique. XYIF TUILES $F = X.y + X.y' \equiv F = X$ 0. 0.0 +0.0 0 TITLE X = 0001+0.1 TUTE Mathmatical expression 1 F=O 1 1.0+1.0 T 13 not unique. X=1 11.1+1.1

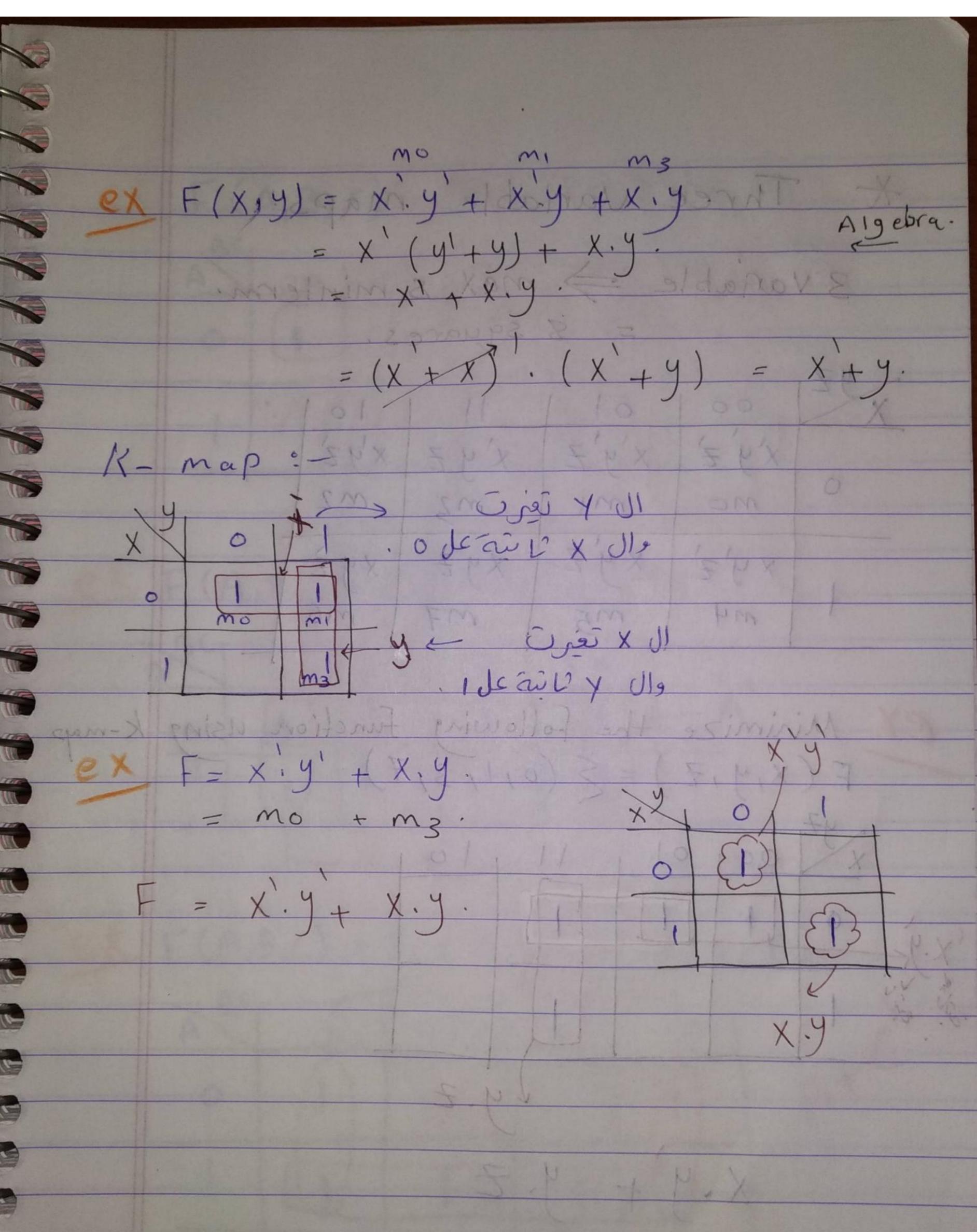


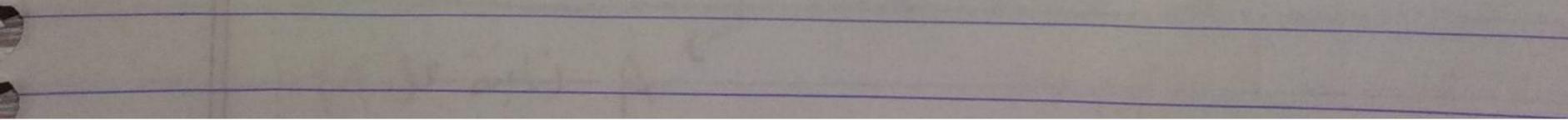


K - map method for minimization. Diagram mad of squares, each square represent one mintern ample xity of digital logic gales is dive Ex Two variables maps. 2 variables => 2 = 4 minterns X'Y' X'Y XY'M3 + 10 x mz mo M LU + V X'Y' x:4 mo X.Y X.4 m3 MZ are the that ton using and are Ch Minimize the following Function. F(X, y) = X, y + X, y + X, y - Alge Sum of mintering. hours dipiontes = mitmaitima =0, Loht, 3, 30 F(X,y) = X'y + X(y+y')=XX'Y + =X PION=21 (X+X) X+4-0 one sietis ina or Variable · ~ In ajo



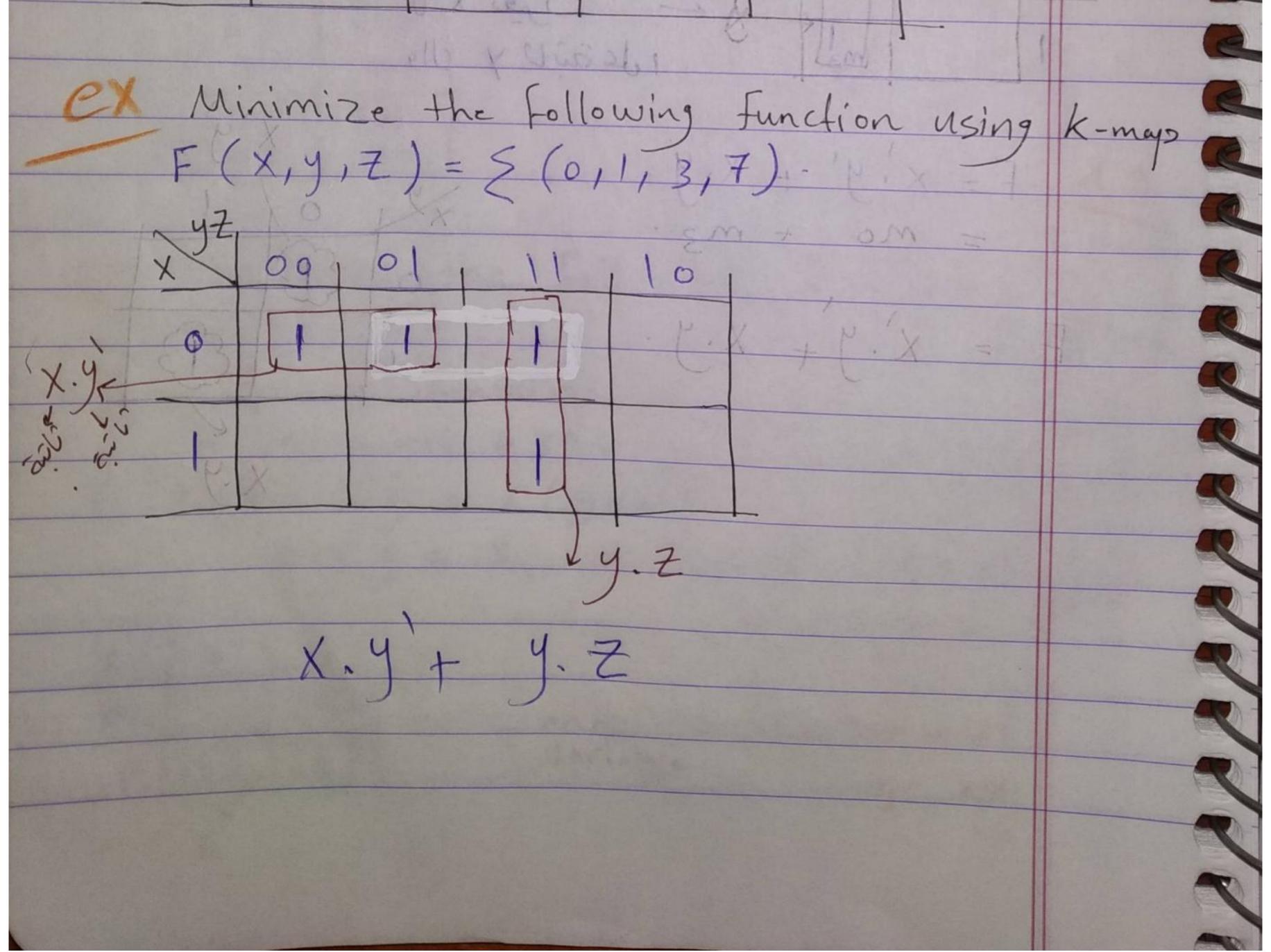






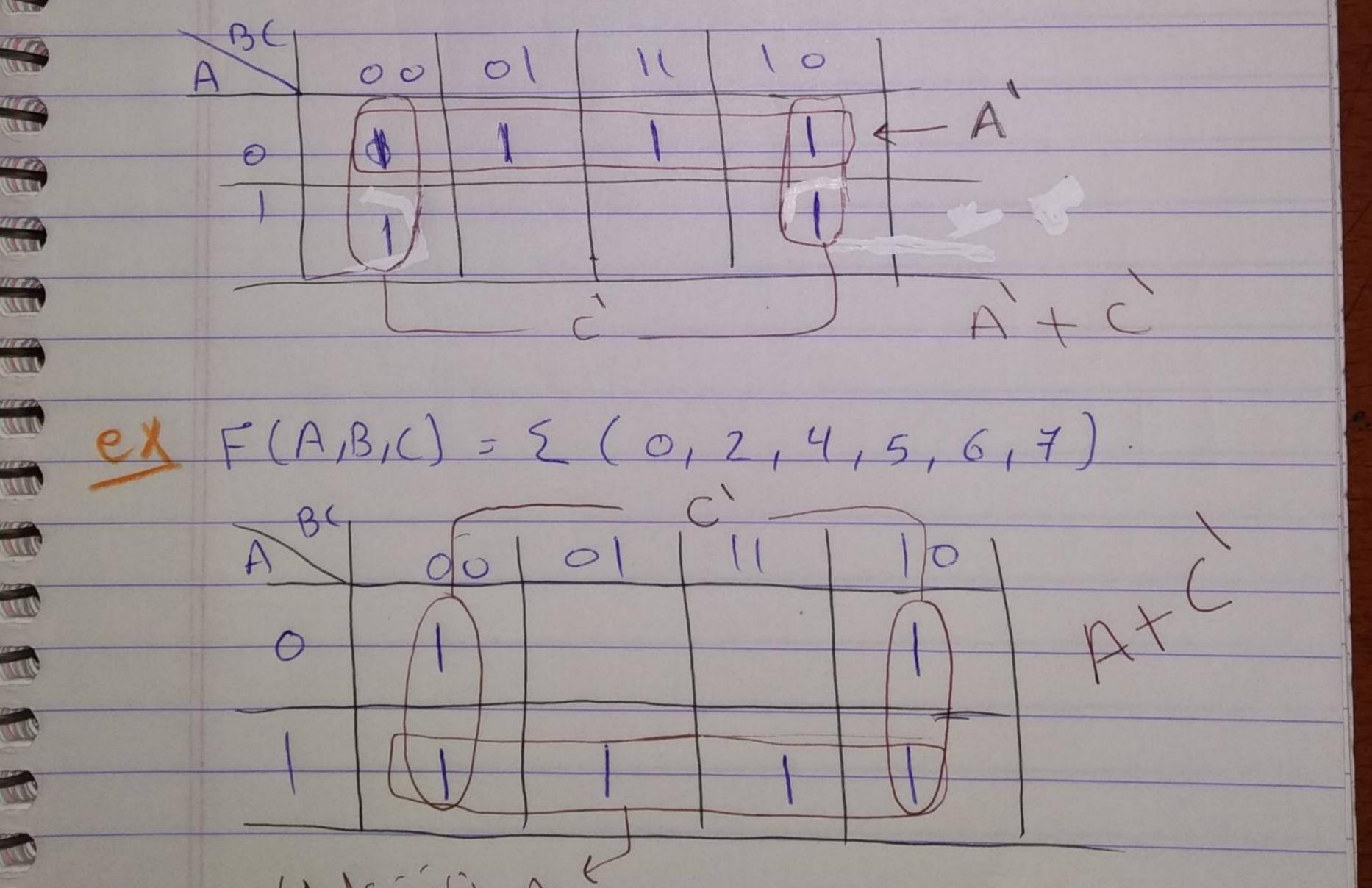


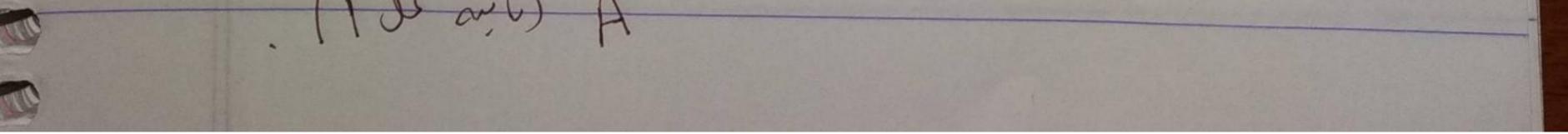
OR 1.Pr. SIN Three Variable maps: - 119 CDra. 3 variable max & mintern. = 8 squares 00 0 0 XYZ XYZ XYZ 47 map 0 MO m2 mi mz X my m5 m6 MZ



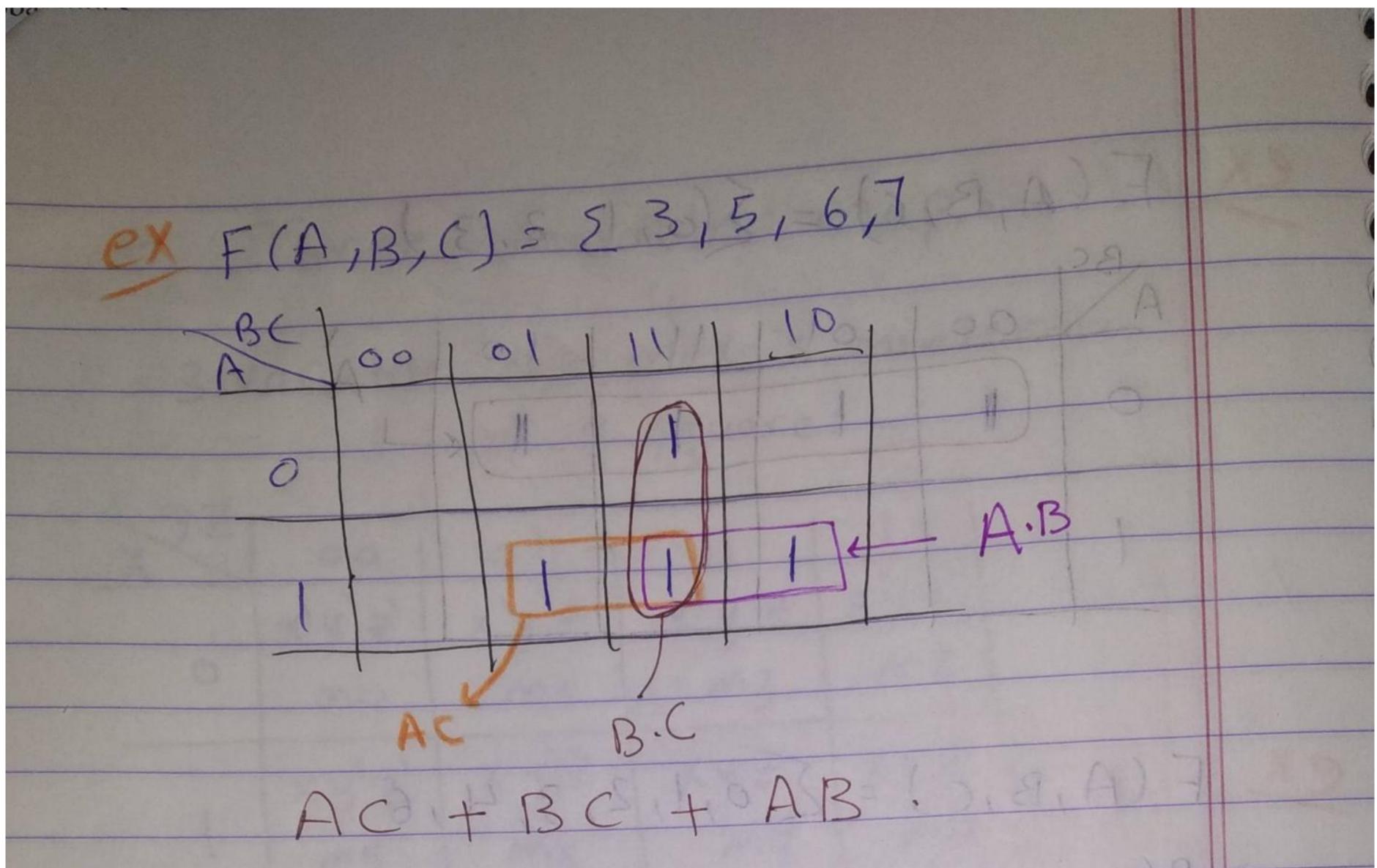


ex	$F(A,B,C) = \{(0,1,2,3), A \}$ $A = \{0,1,2,3\}$ $A = \{0,1,3,3\}$ $A = \{0,1,2,3\}$ $A = \{0,1,3,3\}$ $A = \{0,1,3,3\}$ $A = \{0,1,3,3\}$ $A = \{0,1,2,3\}$ $A = \{0,1,3,3\}$ $A = \{0,1$
ex	F(A,B,C) = E0,1,2,3,4,6

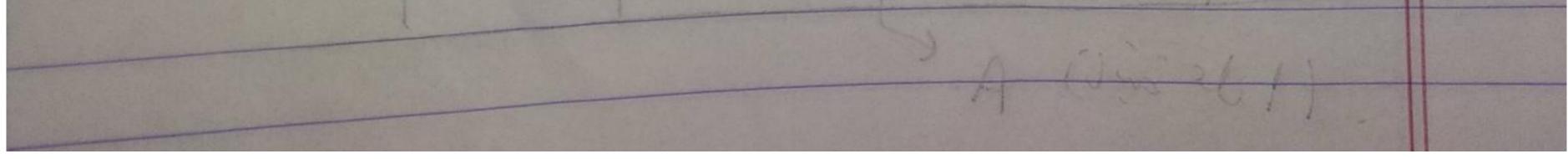






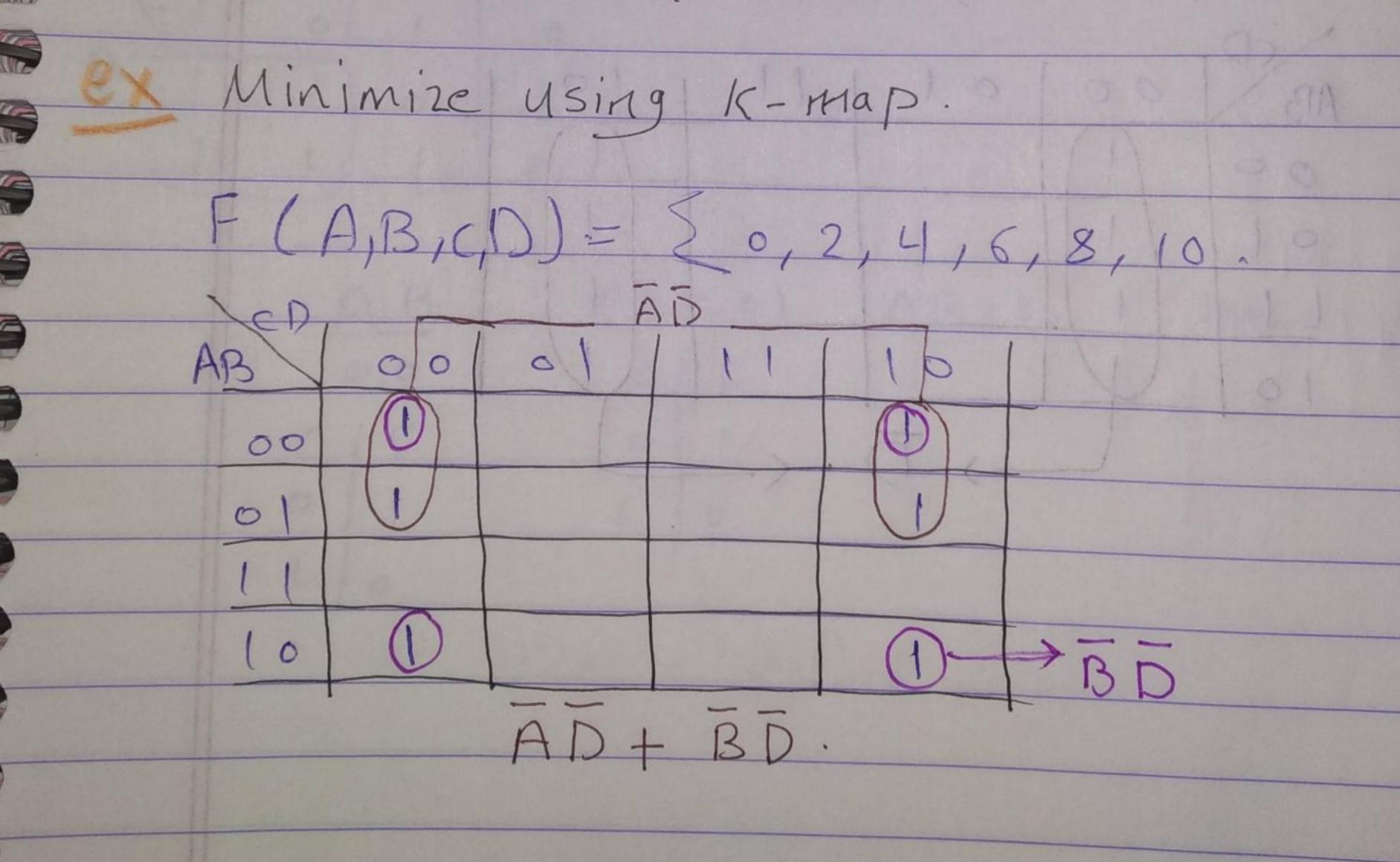


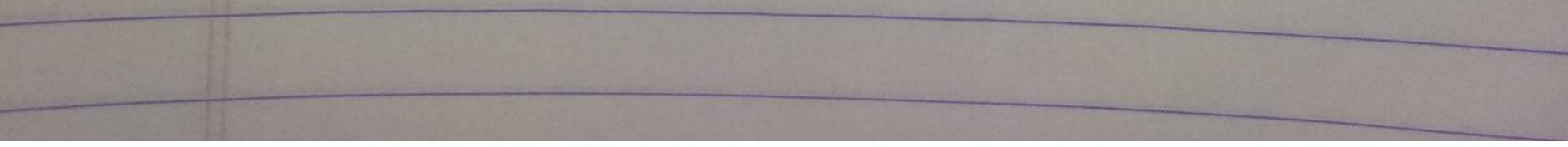
* Four variables maps: F(A,B,C,D) = 4 Variables. 16 minterns = 16 squares. 01 A'B'CD' 0 0 ABCID ABCD ABCD M2 ABCD M6 M3 m mo ABCD ABICD A'BCD' 01 MT ms MH ABCDI AIBCD ABCD ABCD' _____M14___ m15 M13 ABCD AB'C'D AB'CD ABICID MIO mg mll MB





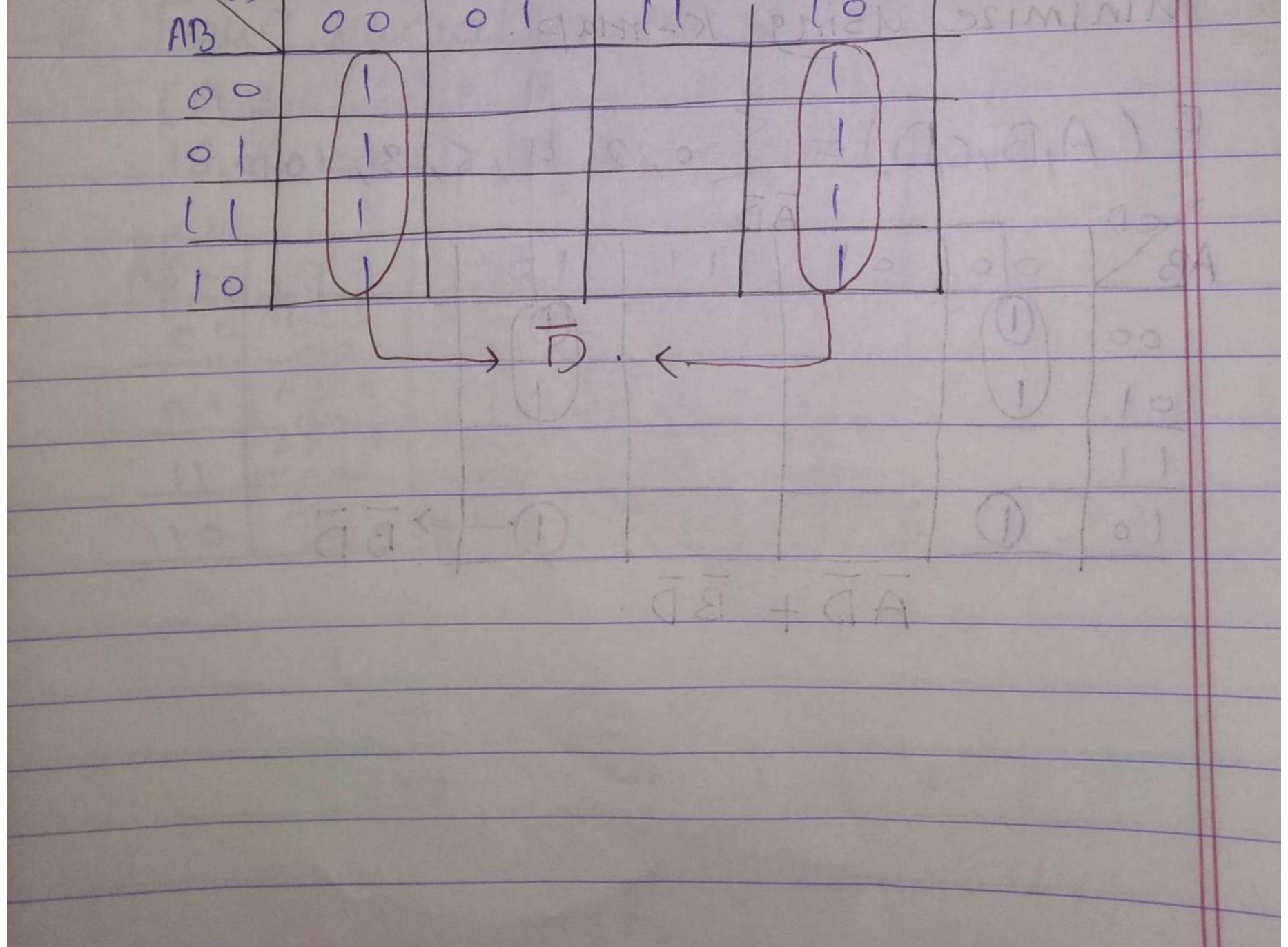
Minimize Using K-map. (A,B,GD) = 12 (0,1, 5,7 4 CP 00 0 1 0 00 ABD 0 0 ĀE





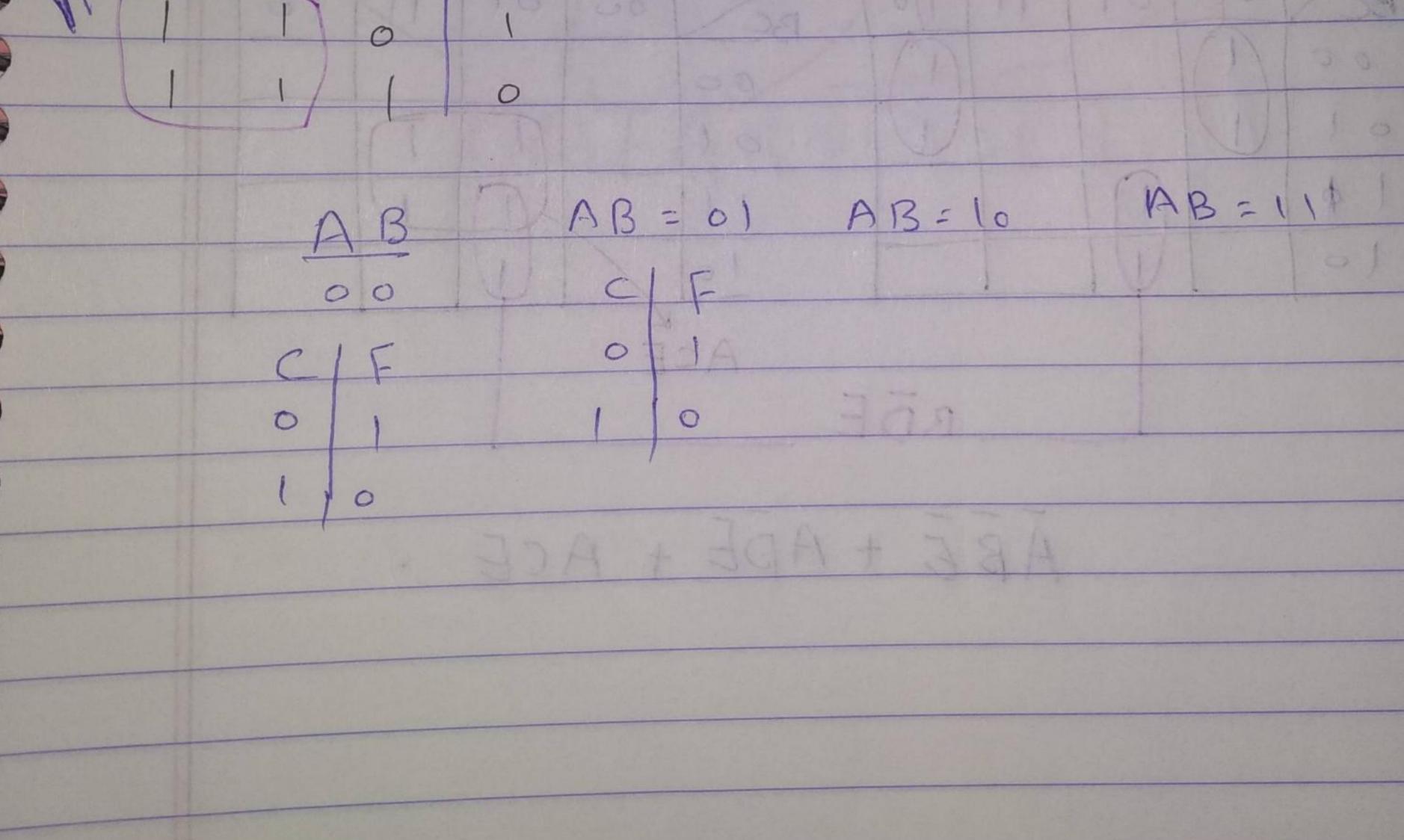


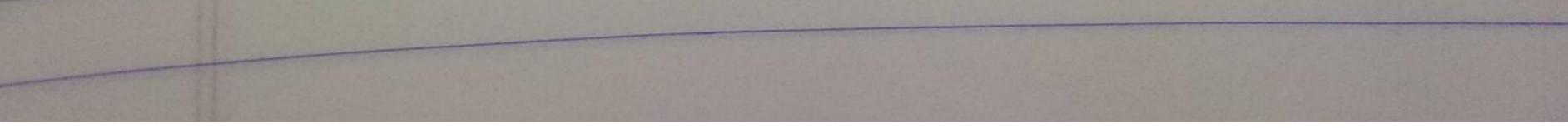
F(A, B, C; D) = So, 1, 2, 3, 5, 7, 13, 15, 9,(C,D) = S(6,2,4,6,8,10,12,14). .





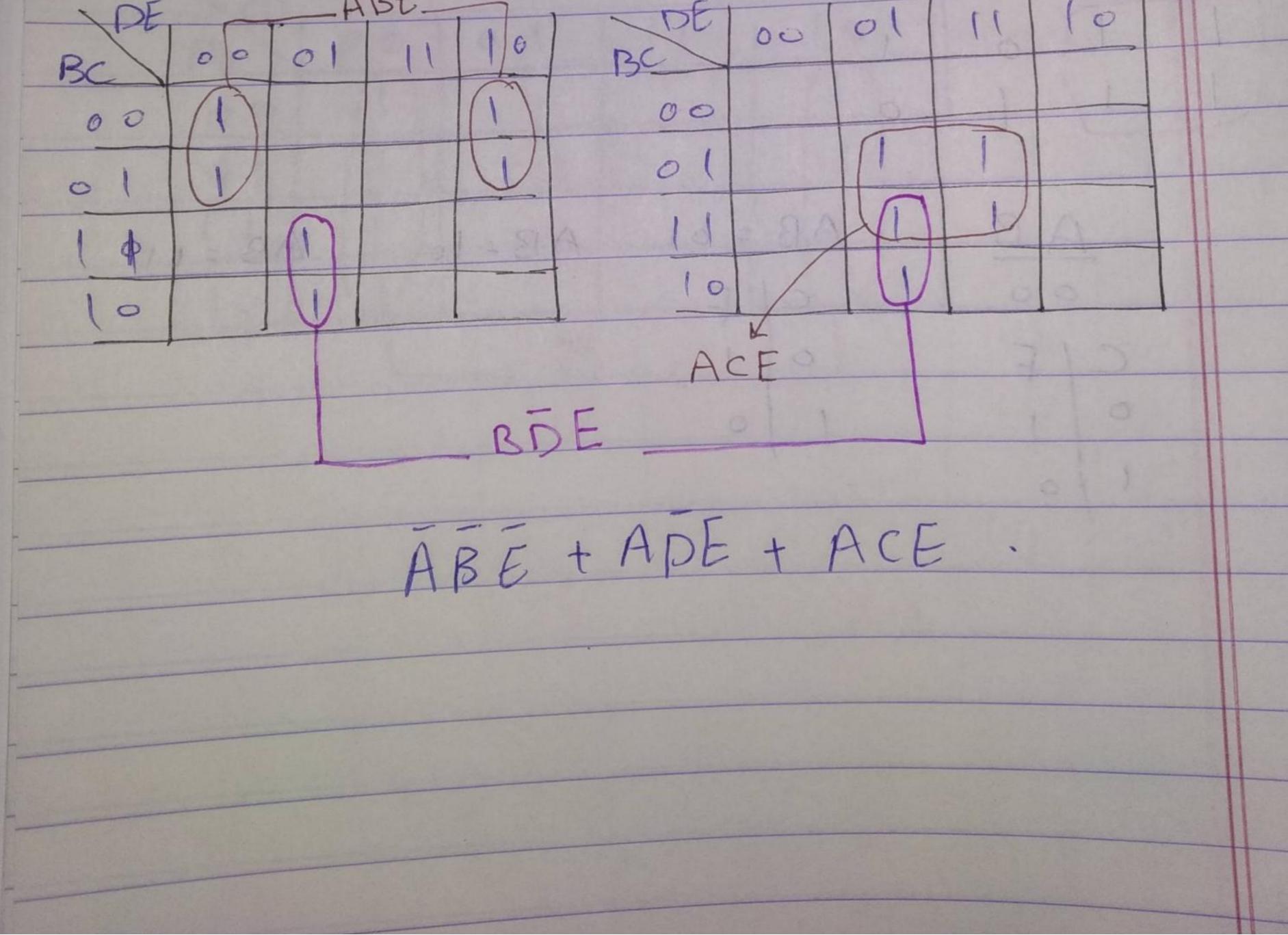
Eive Variables maps = 32 minterns. Note: - F(A, B, C) = 20, 2, 4, 6. 50 11 C R A = 0 0 0 C 0 allB 0 20 0 0 1 21 2 0 0 0 8 0 0 0 Pra 3 00 0 18 0 111 111 22 B 325 0 0 10 0 D 6 0 0 0 1 5 1





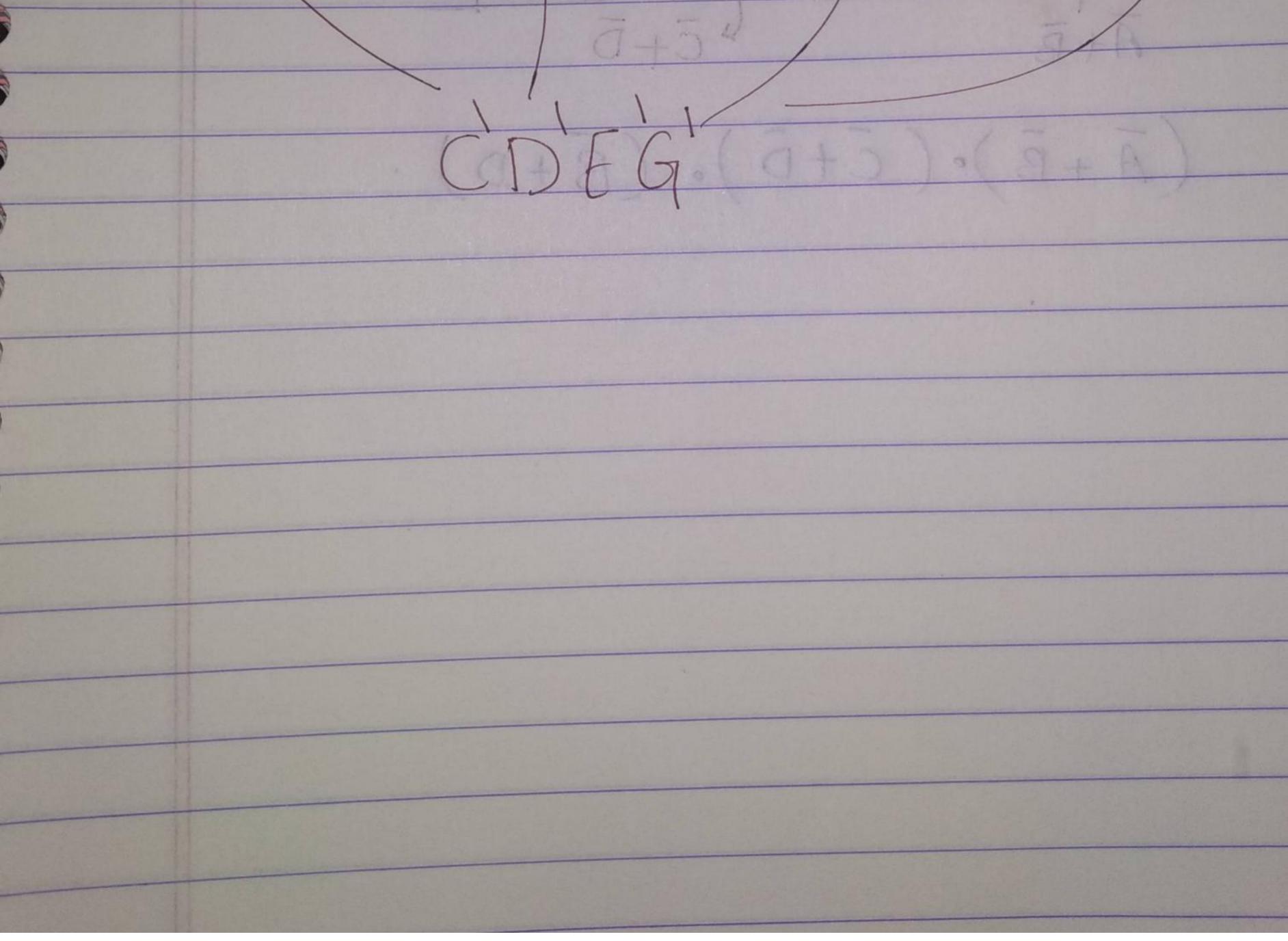


,2,4,6,9) Minimize F(A,B,C,D,E) = 263,21,23,25,29,3. 61 = 0 4 0 DE 20 0 PE D 80 m18 0 BC MIq MIZ M16 ABODE ABCDE 00 00 M22 M3 m M2 M23 MO m21 m20 0 m7 mb 0 M30 m5 M4 M31 m29 m28 ABCDE m14 M13 MIZ M26 m27 M25 M 217 10 10 Ma MS MO MIL ABE



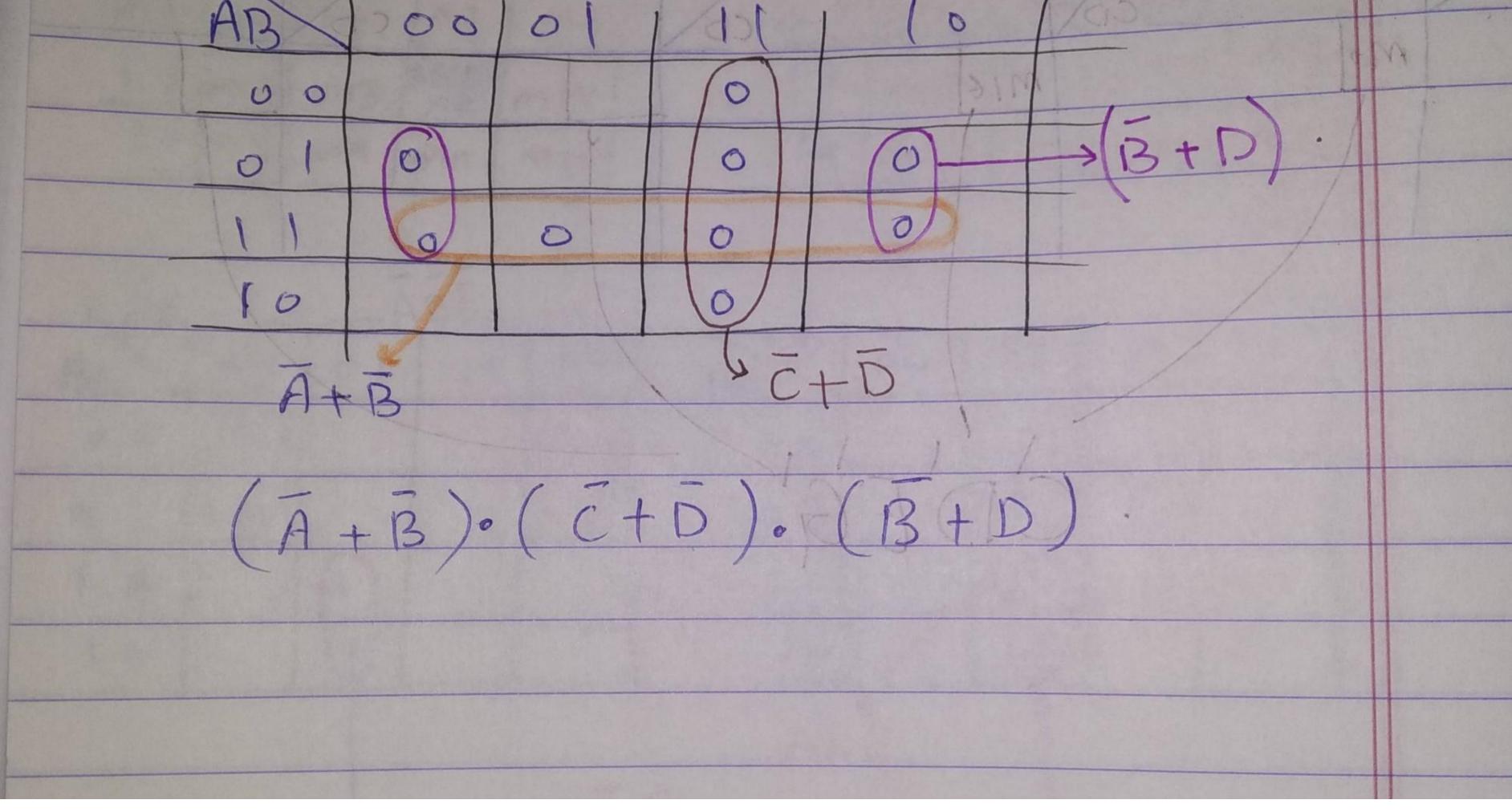


6 variable maps. (auléour) × $F(A, B, C, D, E, G) = \Sigma$ A13= AB=00 AB=01 AB=11 EG CD CD CIT Mo M16 745



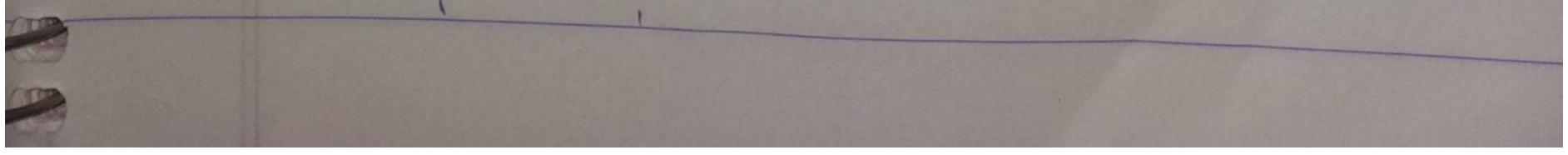


Aproduct of maxtern 3-Simplify the following Function. F(A,B,C,D) = TT(3,4,6,7,11,12,13,14,15)100101101 0



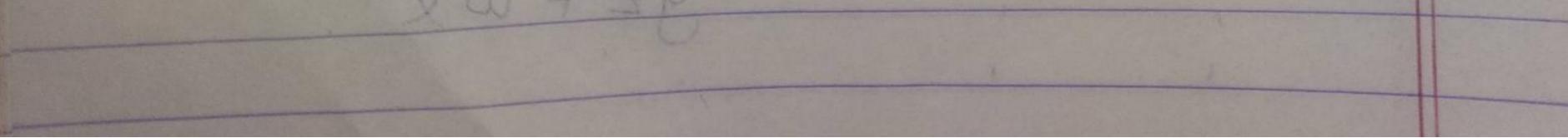


A. * Don't Care Condition 1901 Function that have unspecified outputs For Some inputs. 10111101001 The anspecified mintern of a function are don't Care Condition denoted by X. Sitil's lessential D] Simplify the boolean Function. $F(w, \chi, Y, Z) = \Sigma(3, 7, 11, 15)$ TITL that has don't care condition. TIME . $w_{1X}, y_{1Z} = 20, 2, 5$ WX JZ ioo 1999 0 0 X 00 0 X 0 WZYYZ X- ie vier ist liqueiter elal 0 eisti (gris) sel van 6-7 YZ+ WX



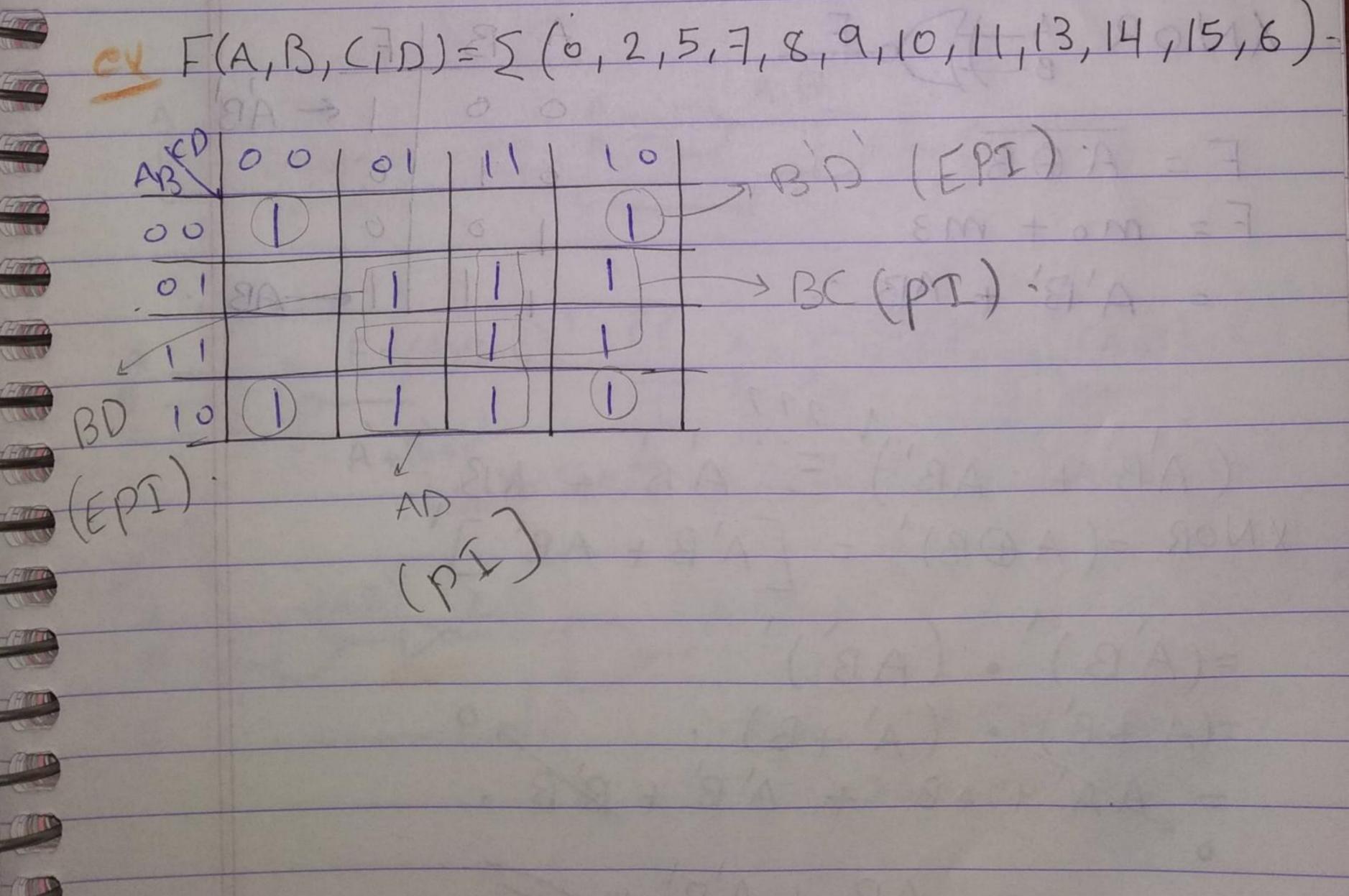


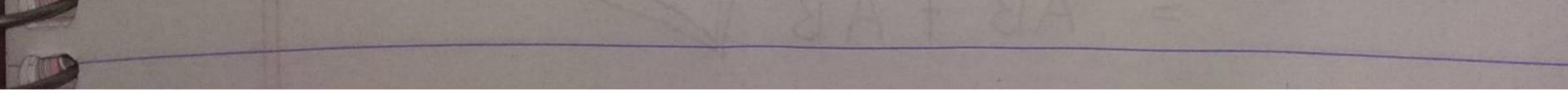
* Prime Implicant (PI) F(A,B,C,D)= 2(0,2,5,7,8,10,13,15) 81 'D' ressential PI 0 1. term 00 term essential 3 B 0 essenti 5,7,13,15 H MHIGNY ND910001 2. F(A, B, C, D) = \$6, 2, 5, 7, 8, 10, 13, 14, 15). > B'D' FEPI 0 0 0 00 >BCEPI] () 0 2 FP CD not essential 0 (L'évéris le chréni) RD RD + BD + BC. 2 elecillane sellican VIII + TH



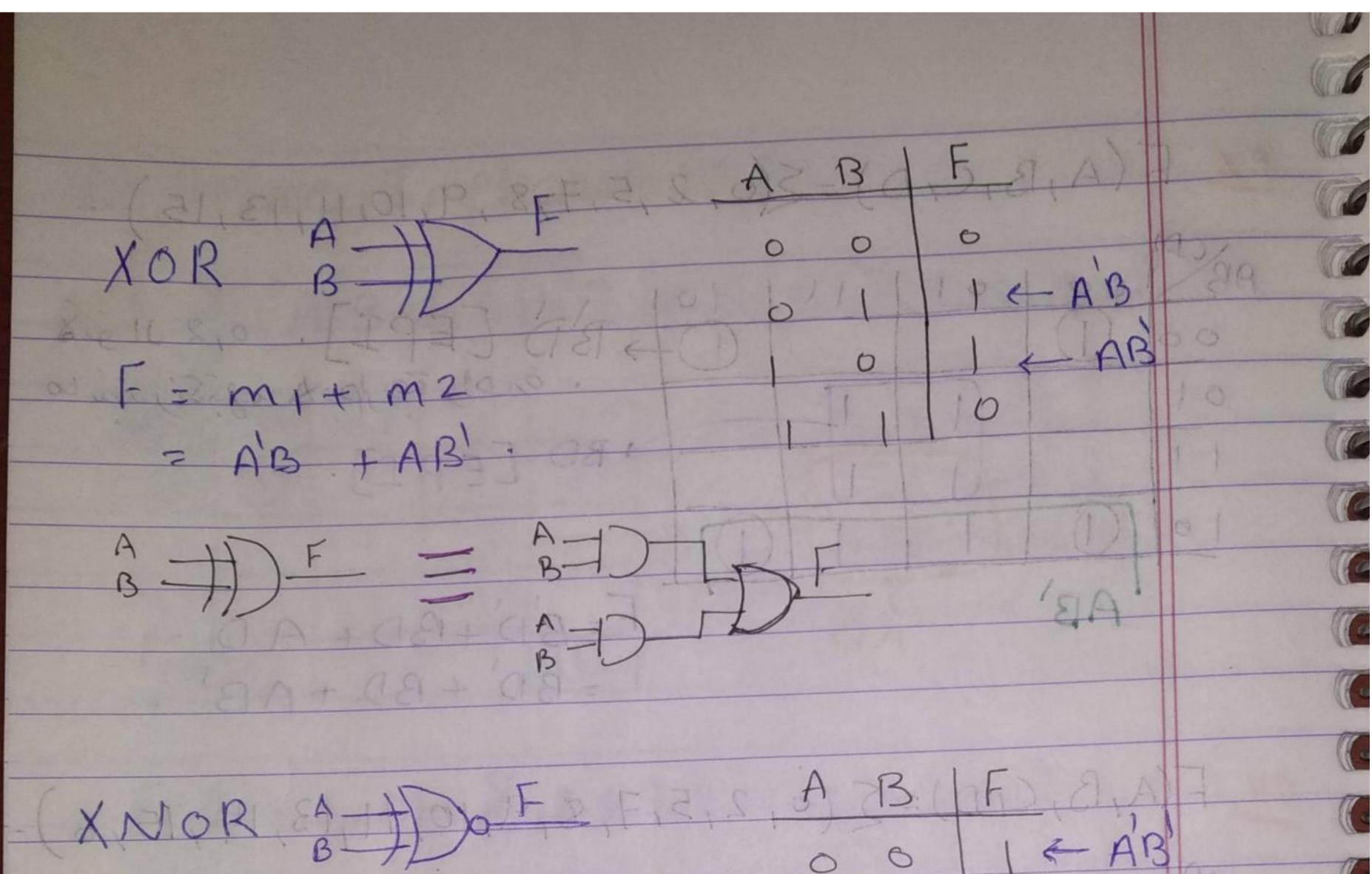


-15 $(A,B,C,D) = \leq (0,2,5,7,8,9,)$ ex لأنوال 2,0 AB 0 0 00 and in fight the first with 00 0 Ener 0 Eller 1 = D'+BD+AB Him HIMA







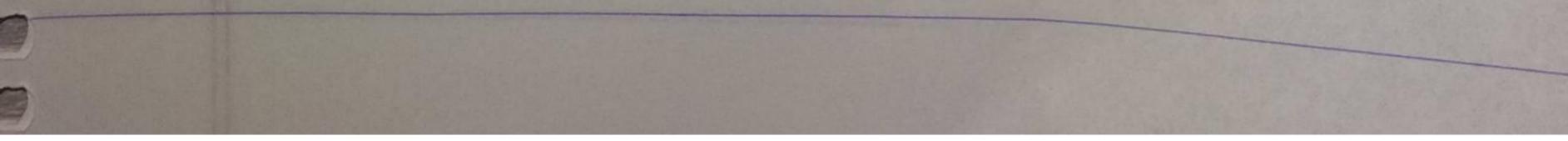


O.C. 6 6 1 F=ABB 0 0 F= mo + m3 I - AB $= A'B' + AB^{-1}$ (AB + AB') = AB + AB:XNOR = (ABB)' = [ABTAB'] = (AB) · (AB) $=(A+B') \cdot (A'+B) \cdot 70$ = AA' + AB + A'B' + B'B. = AB + AB IV 0



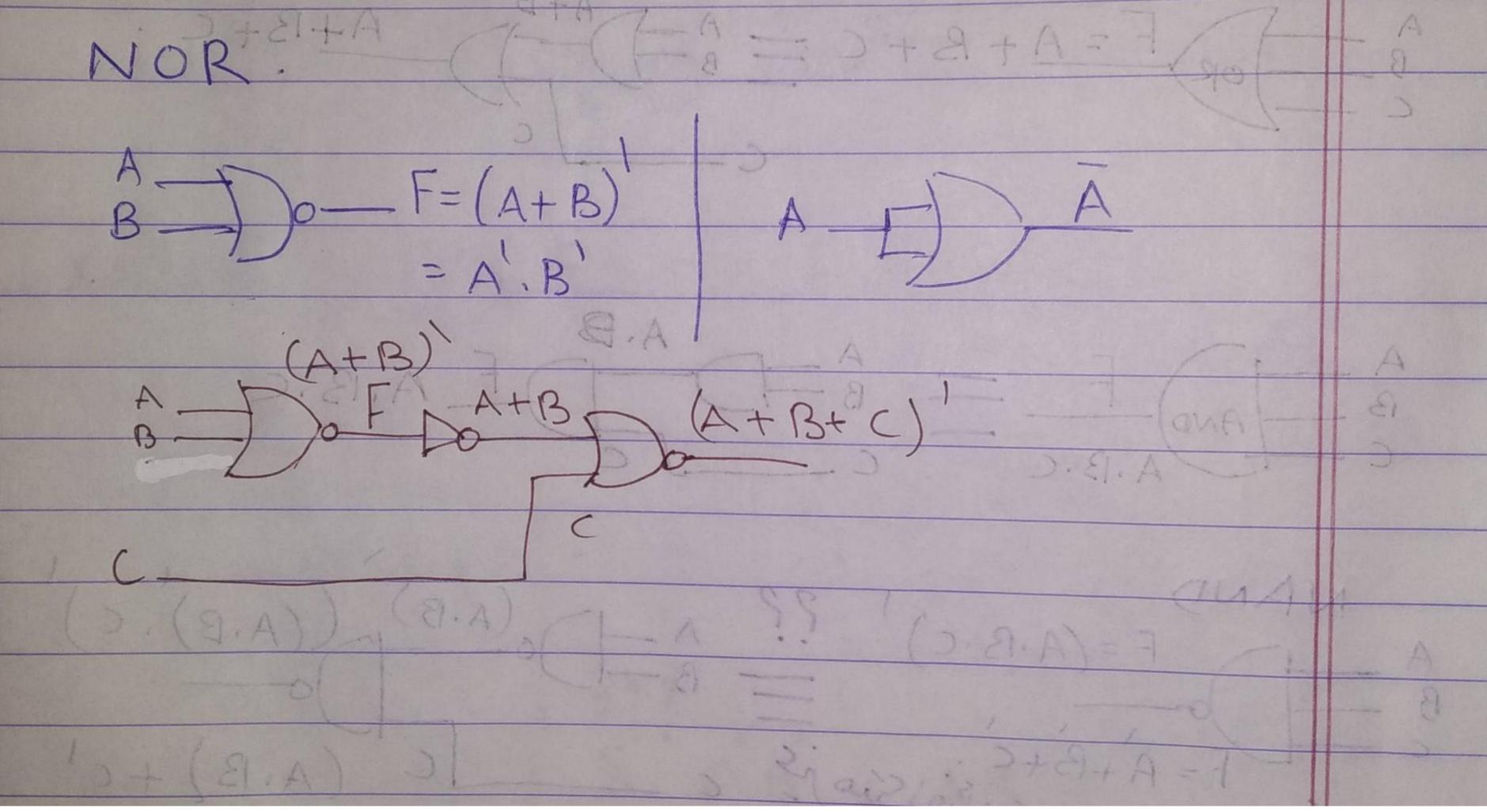


Note All gates are associated except NAND 0R &.4 $(A + B + C) \in R = (A + B) + C$ 3 $\frac{A}{B} = \int_{OR} F = A + B + C = \frac{A}{B} = \int_{OR} F = \frac{A}{B} + \frac{B}{B} = \frac{A}{B} =$ A+B+C B AND A.B ·B·C 11 150 B nver associated



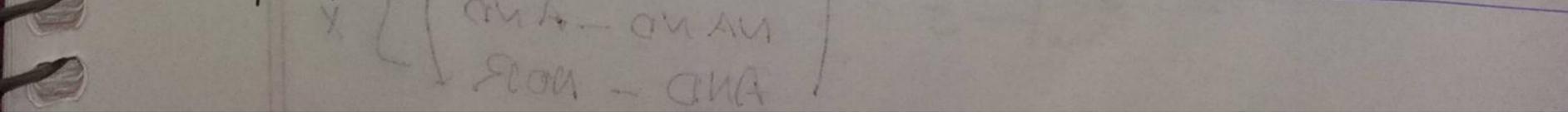


autes are NAND B C (A.B) A·B A B invertor AND going × . SILA



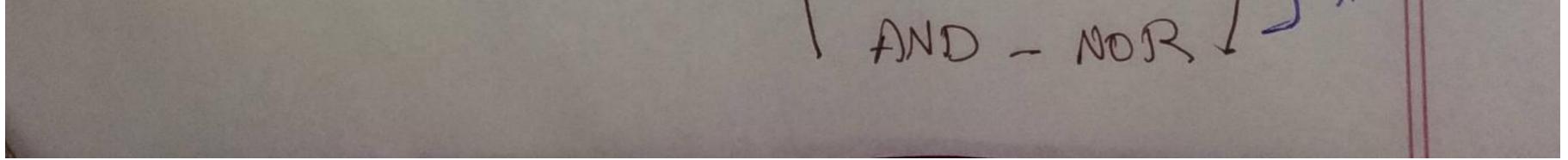


NAND & NOR Implementation. Digital circuits are more frequently constructed with NAND/NOR than and /OR. 79 A (DNAND/NOR use fewer number of transistors. TIMP so Dit's cheaper. 2 less delay. Foutput gates. delay input = (A.B) invertor -> Z $-(A \cdot A) = \overline{A}$ 110 (A.B) Ann (A.B') = 13 GIAN- JUAN Transistor 3- is an Electronic Device anan- 20/ 36 yas, Cul so Lo Jana-20 open circuit/12, SION - SION !!



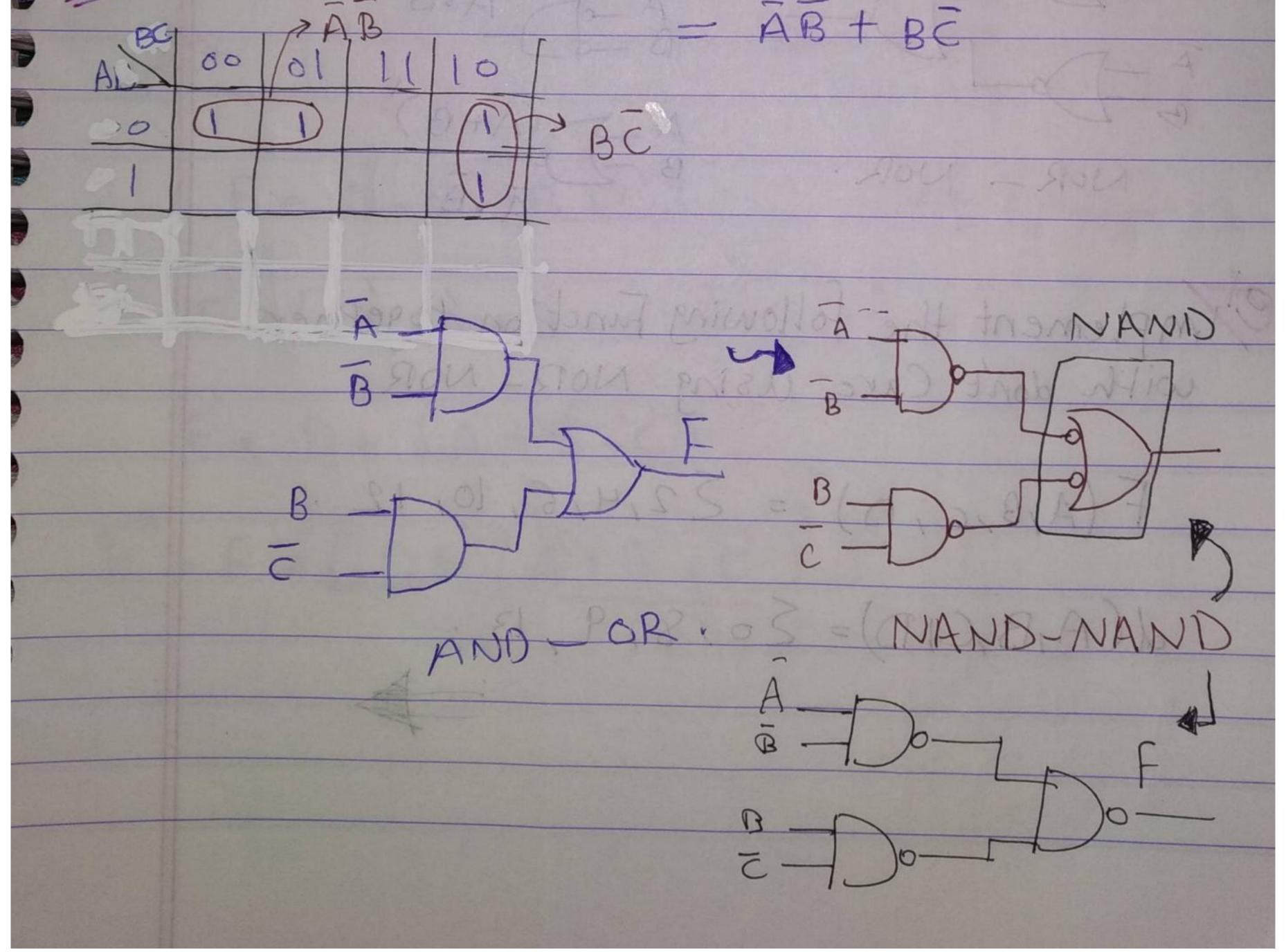


+R) = f nverto 6 0 0 Basic Gates :-6 Form ANIC 0 NAN 8 Form/ degenerate (AND-AND/OR-OR jevel2 8 form/ non degenerate [AND-or NAND-NAND minterns NOR - OR OR - NAND OR-AND NOR - NOR maxterms « NAND - AND



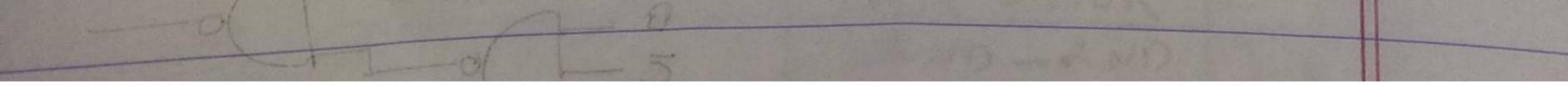


Degenerate :- 1 level d'2 level de des juines Enondegenerate: - 1 level 1 ag d'inici ve ,B,C) = 20, 1,2,6. (mintern. ex

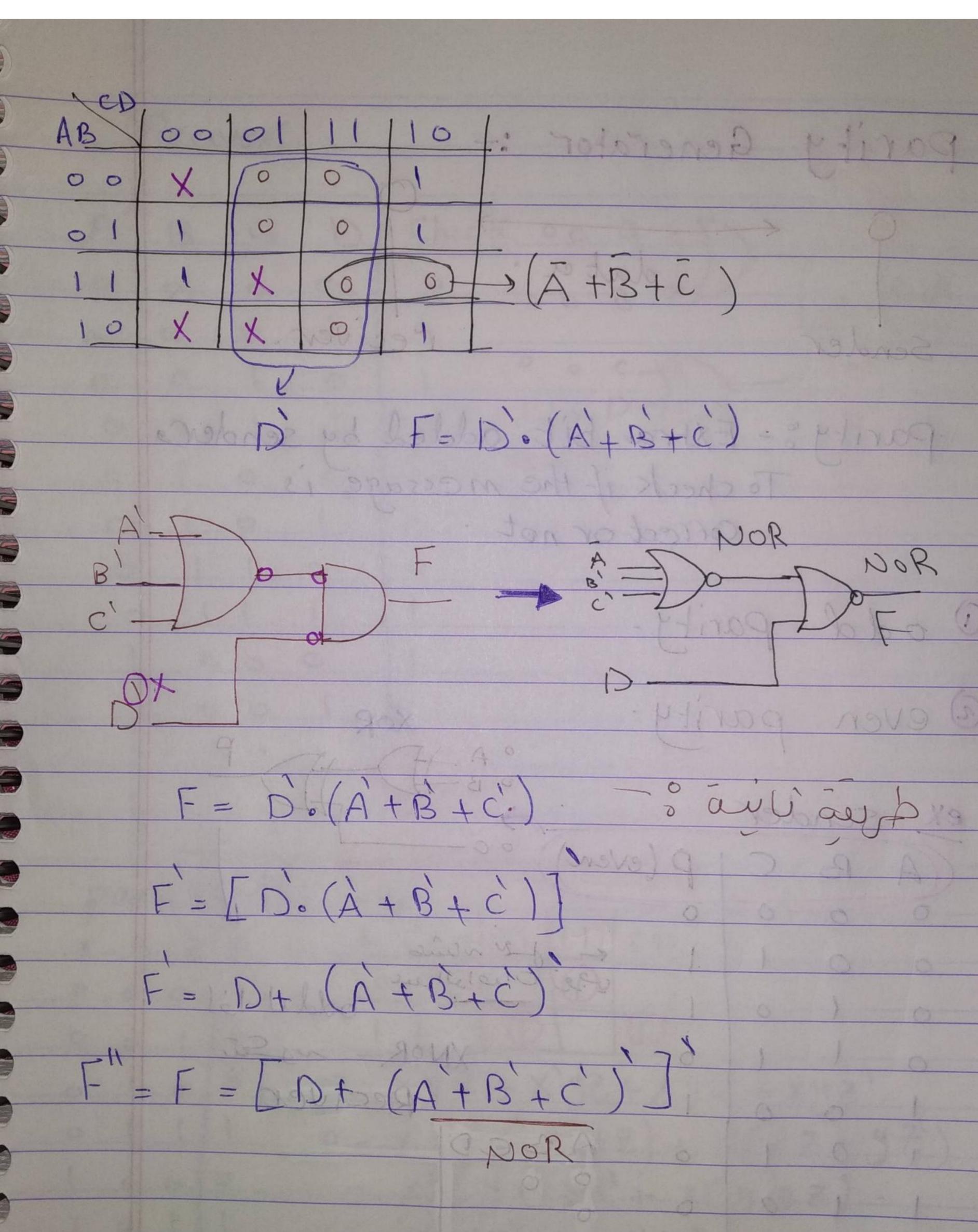


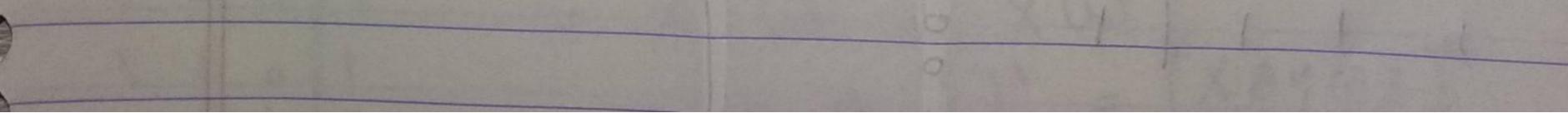


(A, B, c) = 3,4 NOR-00 0 A.B AtB NOR-NOR. Monplement the following Function together with don't Care Using NOR-NOR. F(A,B,C,D) = 22,4,6,10,12. d(A,B,CID)= 20,8,9,13











Parity Generator :dater reciver. Sender Parity: - Extra bit added by sender. To check if the message is correct or not. d parityparit oven 0 Sender even) 0 ~ ~ hins an Ileland is 0 XN OF \circ

