

Faculty of Engineering and Technology

 Department of Electrical and Computer Engineering

 Digital Electronics and Computer Organization

ENCS 2110

**Experiment No. 4 - Digital Circuits Implementation using Breadboard**

pre Lab 4

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Section : 1

3) Design and implement the following circuit using the gates on the chips as shown in Figure 4.1(. Your final circuit must include the ICs, their pin numbers, and the connections between the pins.).

1. Build Full Adder using basic gates.

Using And, OR, XOR chips you can build Full Adder gate like this :



 OR chips And chips XOR chips

