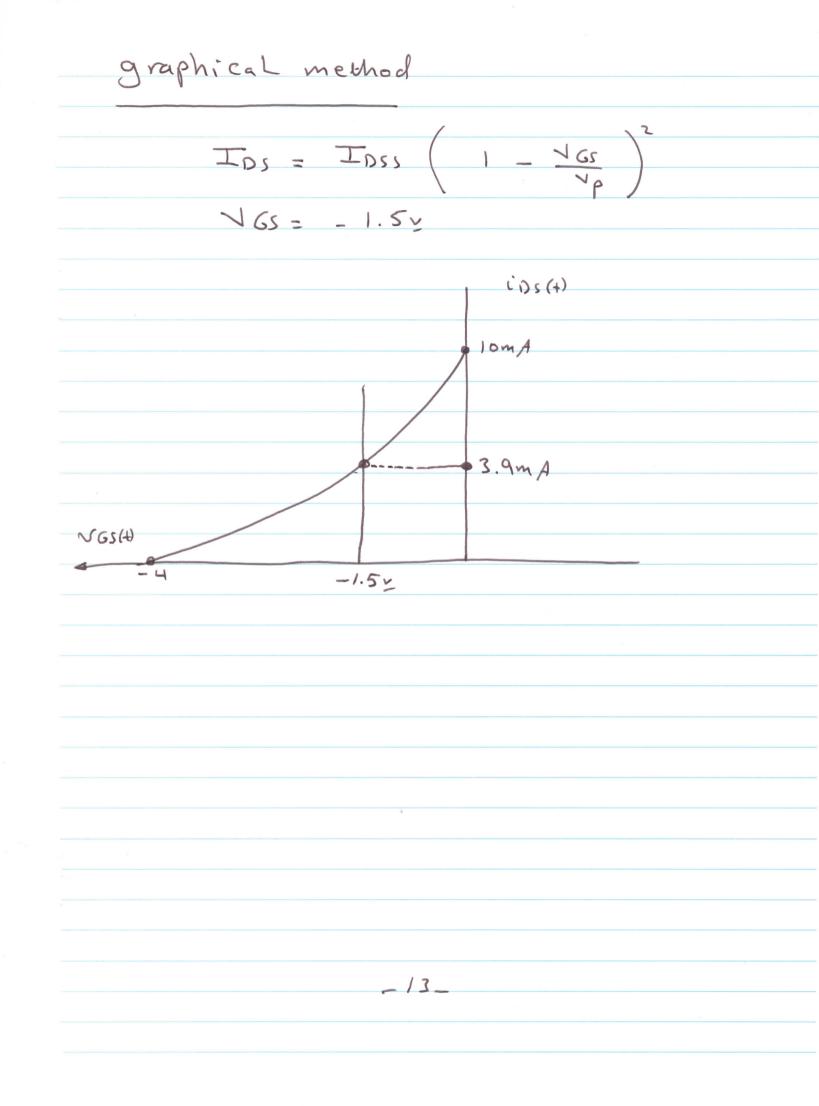
JFET Biasing Circuits 1) Fixed bian Circuit + + DD = +16 v ERD: 2K Tos Np=-42 IDSS = 10mA Rosim - 1.50 Find Q point Since VGS = - 1.5 v, the DFET could be either in the ohmic or pinch off region assume that the JFET is in the pinch off region  $IDS = IDSS \left( 1 - \frac{\sqrt{GS}}{\sqrt{O}} \right)$ NGS = NG-NS = -1.5-0 = -1.5-.: Ips = 3.9 m A -11-

NDD = RD IDS+ NDS .: NDS = 8.2 2 For the JFET to be in the pinch off region |Vps| > |Vp| - |Vgs|> | -4 | - |-1.5 |  $|\forall p_{S}| > 2.5$ Since VDS > 2.52, :. Our assumption is OK -12-Uploaded By: anonymous STUDENTS-HUB.com



Self-bian Civcuit + + DD = + 15 = \$ RD = 1.5K Vp=-42 Rs = 0.6K IDSS = 10mA RG Şim assume that the SFET is in the pinch off region.  $TDS = TDSS \left( I - \frac{NGS}{NO} \right) - O$ VGS = VG-VS NGS = O- RS IDS = \_ RS IDS - @ Sub (2) into ()  $TDS = 10 \times 10$   $\left( 1 - \frac{-0.6 \times TDS}{-4} \right)$ : IDS = 14.77 mA, 3mA Since IDS = 14.77mA > IDSS  $\therefore$  IDS = 3mA.: VGS = -1.8× -14-

VDD = RD IDS + NDS + RS IDS .: NDS = 8.7 Y For the JFET to be in the pinch off region INDSI > INPI- INGS > |-4| - |-1.8|  $|\nabla ps| > 2.2 \times$ : Since | NDS ] > 2.2 - .: the JFET is in the pinch off region and our assumption is ot and TDS = 3.9 m AVDS = 8.7 and VGS = - 1.8% \_15

graphical method  $T_{DS} = T_{DSS} \left( 1 - \right)$ VGS VGS = - (0.6K) IDS When VGS= 0 -> IDS= OMA When NGS = - 34 - IDS = 5 m A IOmA 3mA -1.87 -42 - 16\_

3) Voltage Divider biar Circuit 4 - NOD = - 20 1 Ro \$ 1.85K 1884 ZR, IDS SR2 47K Rs \$ 1.65K IDSS = 18mA Np =+ 5 y NGS IDS = IDSS ( 1 -()NGS - NG-NS  $VG = \frac{47k}{47k+188k} (-20) = -44$  $V_s = -R_s I_{Ds} = -(1.65\kappa) I_{Ds}$ : VGS = - 4 + (1.65K) (IDS) - 2 sub 2) into (), we obtain IDS = 1 4.02mA L 7.4 mA X NDS = - 5.93 Y .17

