Chepter # 5: CPU Scheduling Tet CPU scheduling i Is the process or decision at which Process the OS should select from the READY queue and give to the CPU to execute. [Short-term Scheduling] CPU scheduling READ 0.00 " Short-term" queens It cases to invoke CPU schedulling ? Ilo required. Interrupt Process IIO is completed. > (In synchronous mode Process terminated hundre 4 CPU Schooluling CPU READY Exit Shart-torm avere (D Interrupt [Oroningon OI] 3 In Synchronous mode 1 I O queue SF I10 I/O Completed 10

1 10 11 14 In Corses: 1 + 1 + Non-preemptive. " The of CPU Ilie Citi" T 283 Precuptive " Eic CPU NGIELE Stal & US Zo" -> Our Objective is to: introduce all scheduling algorithms, 9 such that we take in consideration. sving the following criteria! (1) CPU Utilization. (max). (2) Throughput. (max). 3) Tum around True: (min). it's the time from submitting job until it finishs execution. of (4) waiting Time: (min) it's the time the process spends in the READY quelle: 9 (5) Response Time: it's the time from sub withing job until you me ports See the First response from the computer. Weighted Tumanand Time - (minimum is better) 21 .111 Turnerrainal Time Service (CPU) Time. Every Switching From processo (P) to (P_{j}) needs. 2 Context switch.

F Cecture #12 March-6.2018 5 Tuesday F P F Po -2 Contex switch [____ F F F1] FCFS, First Come First Serve: F example: given the following Ready quere: T FF (XOM) Process) Arrival time Service time (CPU burst) 1PI Most OMEO 3 P2 (BAIMOX922010 5 (MAP2) 19194 printing (1) ٢ 200000 PHAR HE QUIT S 4 5 P5 decup 181151 Compute the average timade and time 2 auerage waiting time. 4 we use Gantt diagram 4 -S-Hard 21 MALININ P3 P2 PS Py t 9 8 3 5 13 14 -P3 Ps PA Pu F PB F Pit F P2 P1 PE F 4 Ar Ar Ar Ar Ar

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Tum around - Finish Time Arrivaltime. Waiting time = Turn around time _ Service (CPU) time. Average turn around time = (3-0) + (8-2) + (q-4) + (13-5) + (14-8) = 5.6 unitAverage waiting time = $(3_03)_+(8_2-5)_+(9_4-1)_+(13_5-4)_+(14_8-1)_=2.8$ Et FCFS: Convoy problem "CLSSLI Using This ? SISP) examples=), (0-SS), (0-FS) = TTH) SV() SPIDCESS (S) (CPU builtst Tuff P. Pa 51 dot toppeda P3 99 M of MO27, 20 09) SNT (PU barri (conice ONit. P1 P2 P3 O 33 Pa Pa P2 P3 F P F P3 Ar 124th app 20 20 David Min 1412 20 HTT = (1-0) + (6-0) + (27-0)/3 = (40/3)ANT = (1 - 0 - 1) + (6 - 0 - 5) + (33 - 0 - 27)/3 = (7/3)

T T Placess will think pu burst 10 m WORL (UGD) RUPS with bursto with 27 start prich P2 P_1 DY P3 P2 P1 = 32/11 putton 27 33 Rep Pa P3 Pi (d-0-p P, Pi 7A IBI CES- CONVELL PREMIENT CITCU ATT = (27-0) + (32-0) + (33-0)/3 = (92/3)AWT= (27-0-27)+(32-0-5)+(33-0-1)/3=(5913) E2] Shortest Job Firsts The CPU is given to the process with the Smallest CPU burst (service time) example: PRICESS CPU burst PI 24 P2 3 P3 3 1) assum all processes are there at time O, sin and atrive in the same order, (FC 5 8), (d 6 3), (1 0.1) - TIME

 P_2 Pz P1 30 G P2 P1 F * ATT= $\frac{3+6+30}{3} = \frac{39}{3} = 13$ * HWT = 0 + 3 + 6 = 9 = 3Note: Shortest job First gives the minimum (optimal) Solution, that is it gives the minimum whiting times > There are two versions of SJF: (1) Precuptive: if a job arrives at the READY queue with Shortest Remaining CPU burst less than the remaining of the Time First (SRTF) running process, then the CPU switches 3-11)+ (1. 11-2) + (+ to the new anniving process (2) Non-preemptive; if a job arrives at the READY queue with CPV burst less than the remaining of the running process, then the CPU Continues with the running process? 119 then switches to the new anniving PBCcss,

6 16 1 examples Process Arrival time CPU Himer Pa 0 75 Pa #2 9 Pz 4 11/20 Py 5 4 (a) preemptive: PA P2 PB PH PA P2 2 0 4 1C 5 7 11 P P3 P2 Pu P. Pi Ar F Ar Ar Ar P3 + HTT = (16-0) + (7-2) + (10-4) + (11-5) = 12 = 3 North 200 10 Joning 118112 apply (3702) + HWT = (16 - 0 - 7) + (7 - 2 - 4) + (5 - 4 - 1) + (11 - 5 - 4) = 310 equino dei o Fin still H WRIA 2231 I shad (19) MIKN b) Non-Preeuptive ! + 4 P1 P2 P3 PH 5 8 2 4 7 12 ()16 P2 Pg Pz P2 P3 P Pi R Ar Ar Pr Ar

1.5 $\frac{1}{12} + \frac{1}{12} = (7_0) + (12_2) + (8_4) + (16_5) = 8$ 5 S *HWT = (7-0-7) + (12-2-4) + (8-4-1) + (16-5-4) - 18/4-* at a star - By a fait & lat - D 10 at + 1 A -Y (1) Problem ; Star vation 1: Solution : Aging 2 1 Alexan at Hyperses, give the process some Priority-Major Problem: How the OS can decide the tength of the next CPU burst (service time) ???! Solution: the OS can only estimate the length of the H Mext CPU burst. example: Assume the Adup i and the 1st cpu Th= actual length of the nth CPU burst In= estimated Length if the nth CPU burst. take a constant OFWF1 21000 Define the formula: 30000 $Y_{n+1} = \omega * T_n + (1 - \omega) * Y_n$ - N=O-> Yn=1=Yn y W=1 > Yn+1=Th Mr CRU

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Let us expand the equation (+) 170 $Y_{n+1} = w * Tn' + (1-w) * [w * T_{n-1} + (1-w) * [w * (T_{n-2})]$ + (1-w)*[Yn+1= W*Tn+(1-w) *Tn-1+(1-w)2w*Tn-2 + (1-w)3w * Tn-3 * Substitute w= 1/2 $Y_{n+1} = \frac{T_n}{2} + \frac{T_{n-1}}{2^2} + \frac{T_{n-2}}{2^3} + \frac{T_{n-4}}{2^4} + \frac{T_{n-4}}{2^5}$ ecture #13 with the product of the Weelnesday 1 (19) trait gill ... March 7.2018 31 Priority stored in nor 23 gott instruct The CPU is given to the process with the high priority. Every process is given a priority number. Generally, low number means low priority. - System tasks have high priority 1 1 Star > & Green Hernipts) There are Two versions of priority: 4 (a) - (a) preamptive AK = (b) NON- preemptive trat c 1-01 e

X -19 example: given the queue as follows: 10 process CPU burst priority arrival time 5 PA 108 10:00 0 P2 -52 -10:02 2 9 1 Pz 10 St with . 5 10:05 9 Ry 8 100 A Martin Cis pi 4 10:08 9 1. high # - high priority. (a) precuptive P1 Pag P3 P2 PI PH 2 0 5 8 16 24 P2 P Pu P3 P3 P4 PA Ar A F Ar Ar F F (b) Non-preemptile P1 P4 1 P3 P2 0 2 5 10 8 1171 19 24 P2 P Pu P3 P1 P2 P4 P2 Ar F A Ar Ar L F F Problem; Starvation. . Solution; Aging. > As time progresses, Increase the priority

V [4-] Round Robin (RR): It's best designed for time sharing Interaction systems. Each proses is assigned a slice of time called queintum Q, the process rubs soion for this quantum & CPU switches to another process on FC.FS basis. 77 > Q = very Big > FCFS > Q = very small > ?! example, process & Service time P1 58 35283 P2 17° Q-70 Pz 68 48 288 Pu 24 4 P1 P2 P3 PIZ P4 N P3 Py P4 PI Pa PIB 102057 20 37 :77 97 117 121 141 161 164 172 P2 Py h. Then F F William 1012 intoldst PARAL INDIVIO Bike self a 201 QUAU

S 105.18 J. m.M. 5 [5] Multi Level Queues: 5 5 Ready quelle is divideal in to several quelles. -Each queue has its own scheduling Algorithm. 4 Scheduling between queues, that is Itow to distribute CPU time among the quelles PI 4 5 S there are two Algerithms (1) time slice: 191quince Each queue is assigned a Chunk withthe - Slice of OPU time, which scheduled aunonalits processes and question -COL-0 (2) Fixed pristing: antyroly (BD -9 serve all jobs in "System tasks" Then serve all jubs in "interactive" -9 5 Then Serve all jobs in "Botch" 9 -> preemptive : 319 mores 9 US Non-preemptime 5 NO PROCESS Fruit by the OS S high priority 3 400 mils > System tasks RR, Q=100 6 User R 5 100 mils interactive jobs RR, Q-10 ⇒ 9) 20 mils FCFS Batch jobs Problem 8 Starvation.

March 21.2018 100 Burnet Heles G Multi-Level Feedback Queres 1-1 - Ready queue is divided into several queres and the part particles and - The process can more up & down between queress Hipp 11 sele 510 stort el example; Konto ornit (1.) With it is to a least a sure of Wour Port he abur. -Q1PR, Q=10 mils preemptive 100000 \rightarrow Q2 \rightarrow RR, Q=100 mils non preemptive tept unale?" is all i undalog" of all the a Q3-> FCFS > new process enters Q1 example: Process CPU-burst 20 Pi 11/11 220209 25 Pa 021/160 ZX2 CP3 Makel 120301-02 80 P4 A YDEN 8 61 6 97 era MA 30 1 Astro Jean antomate supplies 11

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R P_2 R4 P3 Pi P3 P2 R P2 P3 10 Ó TOS 20 30 PI 38 53 253 153 302 313 The second P2 P4 P P PJF S Ŗ F Ar Y S E Algorithm Evaluetion: T (1) Deterministic model pour F (2) Quering Theory "theoritical" (3) Simulation "good" * (4) Implementation "best Algorithm for engluertion" -3

Chapter 6

Concurrent Processes and Process Synchronization

Concurrent Processes

Concurrent process and either independent or cooperating Independent process : can't affect or be affected by the processors

Precedence Graph:

Given the following statements:

- (1) $a = x + y \le i$ (2) $b = z + 1 \le 2$
- (3) $-c = a b s_3$

(4) $w = c + 1 \zeta_{4}$

Clearly,

statements (3) & { (1) or (2) } can't executed concurrently.

(4) & (3) can't executed concurrently.

(4) & { (1) or (2) or (3) } can't executed concurrently.

But statements (1) & (2) can be executed concurrently.

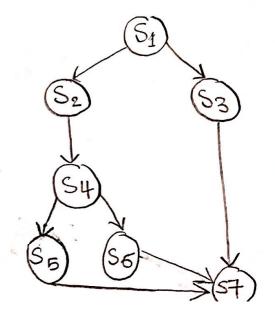
- So if we have multiple functional units in our CPU such as adders or we have multiprocessor system then statements (1) & (2) can be executed concurrently (in parallel).

(1)

Definition: A precedence graph is a directed graph whose nodes correspond to statements. An edge from node S_i to node S_j means that S_j is only executed after S_i .

In the given graph:

- $S_2 \& S_3$ can be executed only after S_1 completes
- S_4 can be executed only after S_2 completes.
- $S_5 \& S_6$ can be executed only after S_4 completes.
- S_7 can be executed only after S_5 , S_6 , S_3 completes.
- S_3 can be executed concurrently with S_2 , S_4 , S_5 , S_6 .



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Concurrency Condition

- How do we know if two statements can be executed concurrently and produce the
- Define: .

 $R(S_i) = \{a_1, a_2, ..., a_m\}$ be the **READ** set for statement S_i , which is the set of all variables whose values are referenced by statement S_i during execution. $W(S_i) = \{b_1, b_2, ..., b_n\}$ be the WRITE set for statement S_i , which is the set of all variables whose values are changed (written) by the execution of statement S_i

Examples : Given the statements:

S:
$$c = (a - b)$$

R(S) = {a, b}
W(S) = {c}

S: w = c + 1 $R(S) = \{c\}$ $W(S) = \{w\}$

- S: x = x + 2 $R(S) = \{x\}$ $W(S) = \{x\}$
- S: read(a) $R(S) = \{a\}$ $W(S) = \{a\}$

* S: read(a)

$$R(S) = \{a\}$$

 $W(S) = \{z\}, p$

The Bernstein's conditions for concurrent statements are:

Given the statements $S_1 \& S_2$, then $S_1 \& S_2$ can be executed concurrently if: $R(S_1) \cap W(S_2) = \emptyset$

 $W(S_1) \cap R(S_2) = \emptyset$ $W(S_1) \cap W(S_2) = \emptyset$

Example:

Given, $S_1: a = x + y$ $S_2: b = z+1$ $R(S_1) = \{x, y\}$ $W(S_1) = \{a\}$ $R(S_2) = \{z\} \downarrow \flat$ $W(S_2) = \{ b \}$ $\{x,y\} \cap \{b\} = \emptyset$ $\{z\} \cap \{z\} = \emptyset$ $\{a\} \cap \{b\} = \emptyset$ **Example:** Given, $S_3: c = a-b$ $\mathbb{R}(S_3) \cap \mathbb{W}(S_2) = \{a, b\} \cap \{b\} \neq \emptyset$

Fork & Join Constructs:

- Precedence graph is difficult to use in Programming Languages, so other means . must be provided to specify precedence relation.
- The Fork L instruction produces two concurrent executions. One starts at statement labeled L-> Ocheb.
- Other, the continuation of the statement following the fork instruction

Example: The programming. segment corresponds to the precedence graph is:

S₁; Fork L; S. : Concurrent L: S3; (*) When the fork L statement is executed, a new computation is started at S3 which is executed concurrently with the old computation, which continues at S2. That is, the fork statement splits one single corporation into two independent computation;

Astoniument The join instruction recombine two concurrent computation. Each computation must ask to be joined. ananan (52

Computation

to cont Since the two computations executes at different speeds, the statement which executes the join first is terminated first, while the second in allowed to continue.

- For 3 computations, two in terminated while the third continues.
- If count is number of computations to join, then the execution of the join has the join instruction (function) Calif effect
 - be executed concurrently, but, one proces at a time.

Un:

If count \neq • then quit (quit this computation) The join statement for two computations is executed atomically, i.e. can't be executed concurrently but in a sequential manner, because this might affect count giving a wrong result.

For example, if both decrement count at same time then count = 0, and the computation dues not quit.

For two processes:

hence the name Fork

Count =2 Fork L1;

count = count - 1;

Gount = # of computations to join;

if (count 1=0) Quit (stop) computation;

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Fork

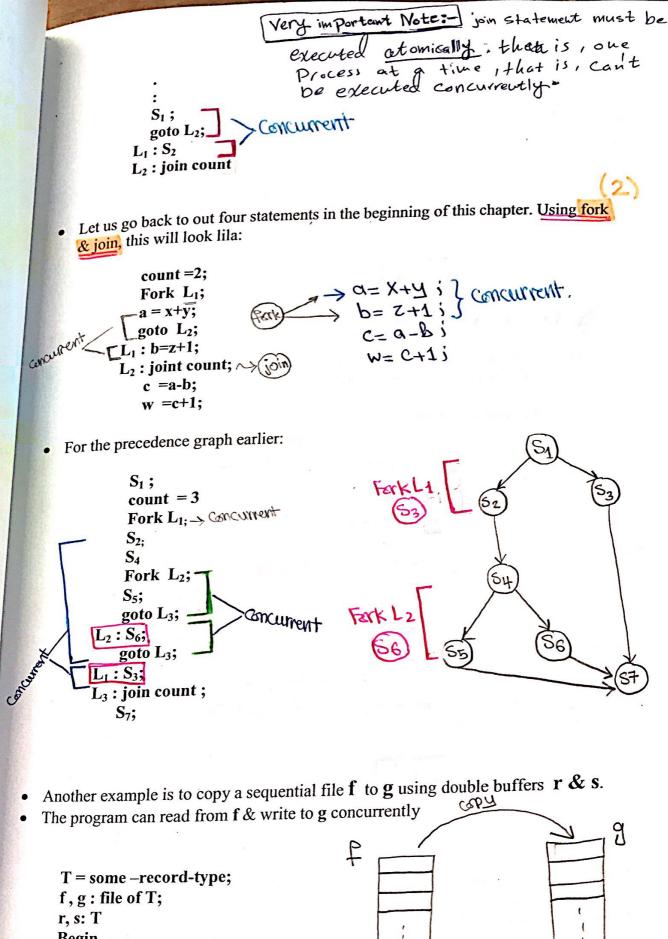
function join ;

Count = count-1;

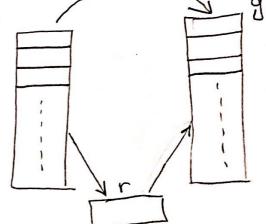
Count=3220

32

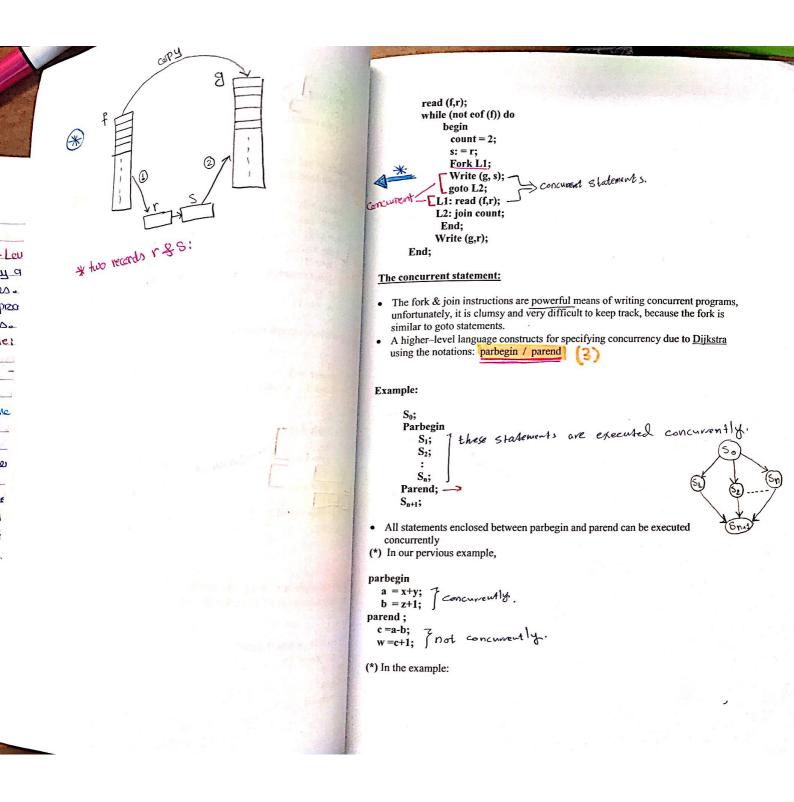
25



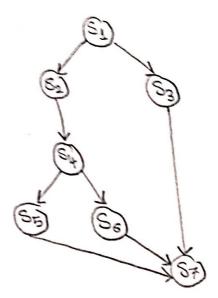
Begin reset (f)



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```
Si:
parbegin
                               3
  S.;
   begin
     S2;
     Se:
     parbegin
       Ss;
              Concurrent.
       Se:
    parend;
  end;
parend;
S7;
(*) For the files copying files :
begin
  reset (f);
  read (f, r);
```



reset (f); read (f, r); while (not eof (f)) do begin S = r; \rightarrow parbegin write (g, s); \mathcal{L} concurrent read (f, r); \mathcal{L} concurrent \rightarrow parend; end; write (g,r); end;

Process Synchronization

Background

Process Cooperation

- o Information Sharing
- o Computation Speedup
- o Modularity
- o Convenience

Example : Producer-Consumer problem, the bounded buffer problem: Data Structure used:

```
item . . ; //can be of any data type
item buffer[n], nextp , nextc;
int in = 0, out = 0;
```

<pre>do { produce an item in nextp while ((in+1)%n ==out) no-op; // full buffer buffer[in] = nextp;</pre>	<pre>do { while (in == out)</pre>
<pre>in = (in + 1) % n; } while true;</pre>	<pre> } while true;</pre>

- Shared memory solution to bounded buffer problem discussed before allows at most n - 1 items in buffer at the same time.

- Suppose that we modify the producer consumer code by adding a variable *counter*, initialized to 0 and incremented each time a new item is added to the buffer, and decremented each time an item is taken from the buffer.

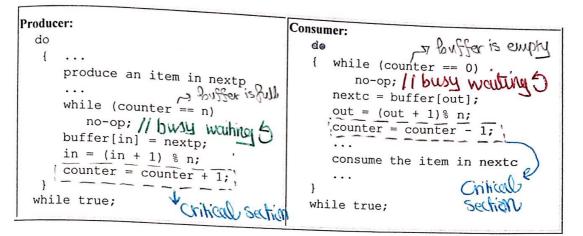
Bounded-Buffer

Data Structure used:

```
item . .; //can be of any data type
item buffer[n], nextp , nextc;
int in = 0, out = 0;
int counter = 0;
```

-* with Counter

* with Counter



• Counter = counter + 1; could be implemented as

register1 = counter producer register1 = register1 + 1 counter = register1 Concurrently • Counter = counter - 1; could be implemented as Consumer register2 = counter register2 = register2 - 1 counter = register2• Consider this execution interleaving: S0: producer execute register1 = counter $\{register1 = 5\}$ S1: producer execute register1 = register1 + 1 {register1 = 6} S2: consumer execute register2 = counter $\{register2 = 5\}$ S3: consumer execute register2 = register2 - 1 {register2 = 4} S4: producer execute counter = register1 $\{\text{count} = 6\}$ S5: consumer execute counter = register2 $\{\text{count} = 4\}$ • No problems if there is a strict alternation of the consumer and **producer** processes Consumer 109 Droghos Rea 1 ounter 6

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